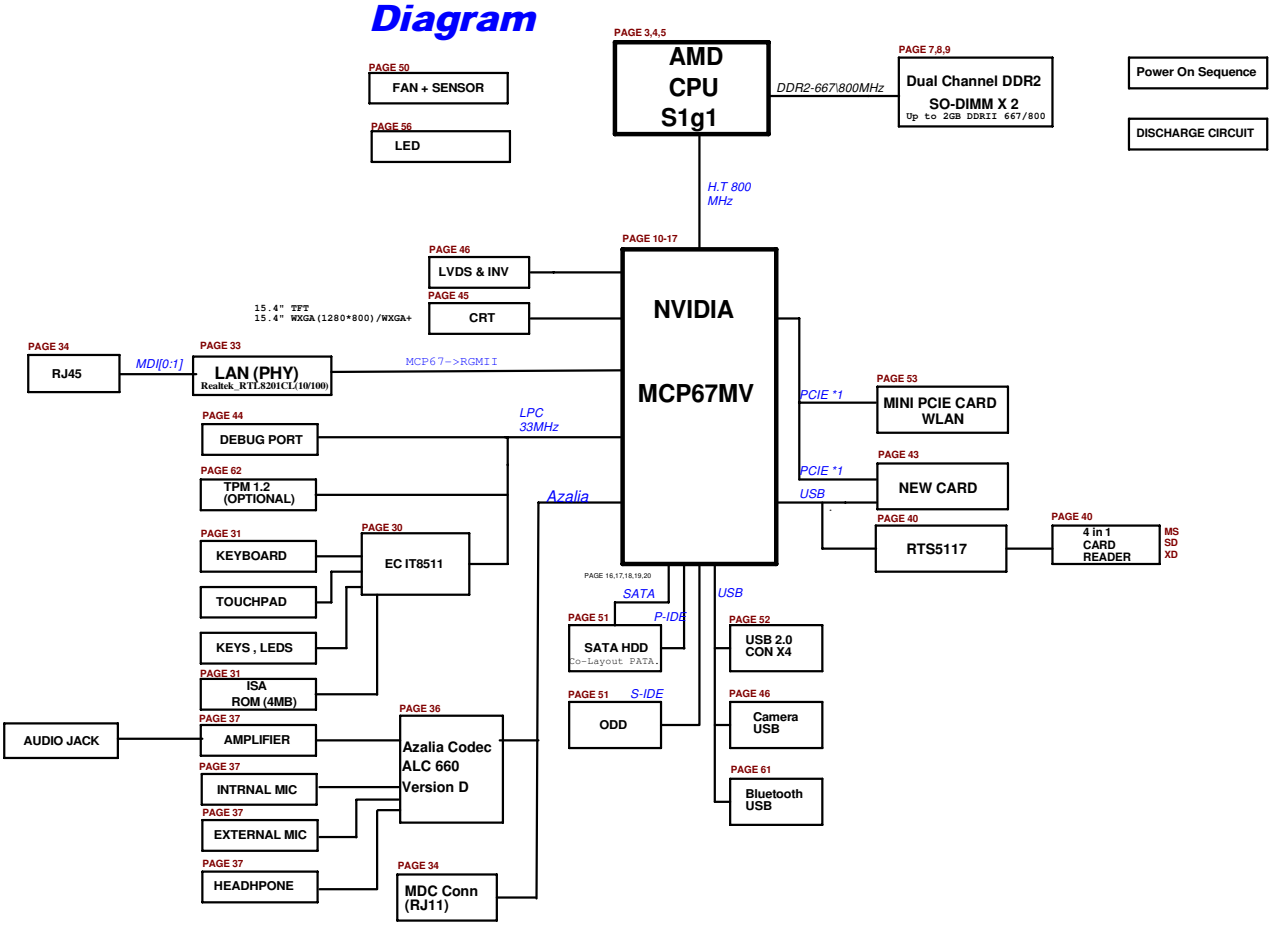
F5N Block Diagram



Title : F5U
ASUSTeK Computer INC
Engineer: He_Wang
Size: C
Project Name: F5U
Rev: 1.0
Date: 星期二, 十月 23, 2007
Sheet 1 of 88

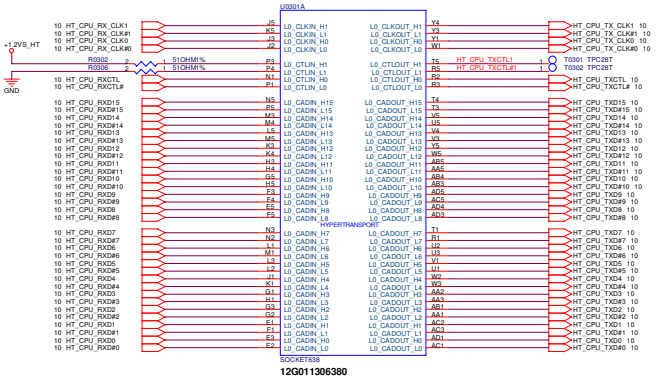
ASUSTeK Computer INC

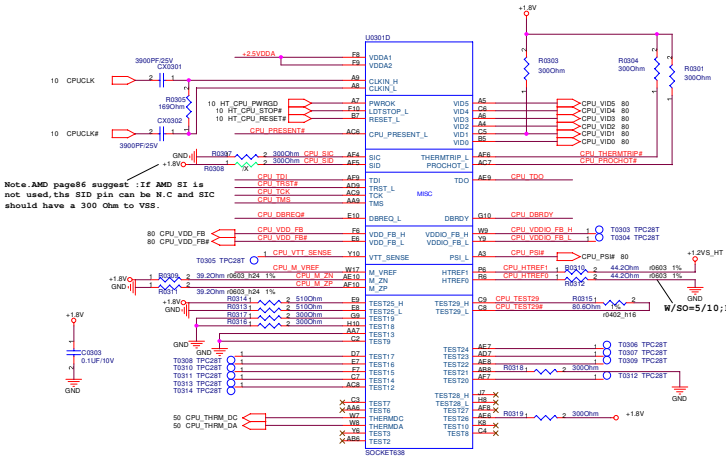
Title : F5U

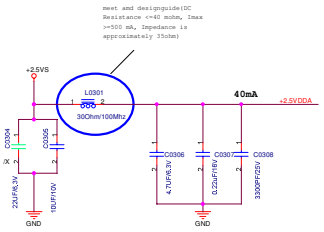
Engineer: He_Wang

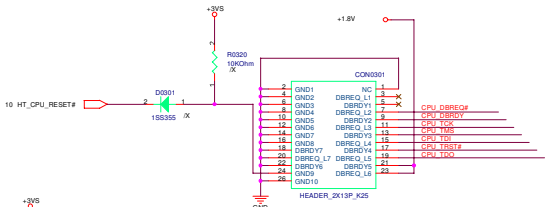
Size	Project Name	Rev
C	F5U	1.0

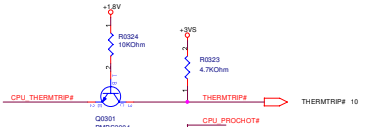
Date: 星期一, 十月 08, 2007 Sheet 2 of 88

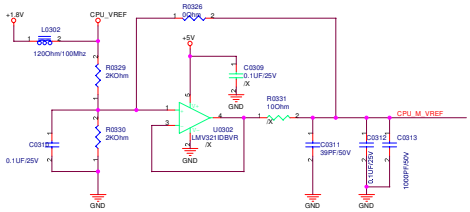


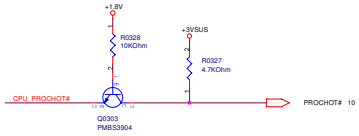


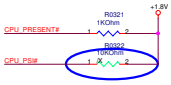


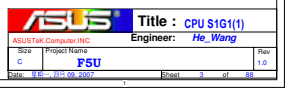


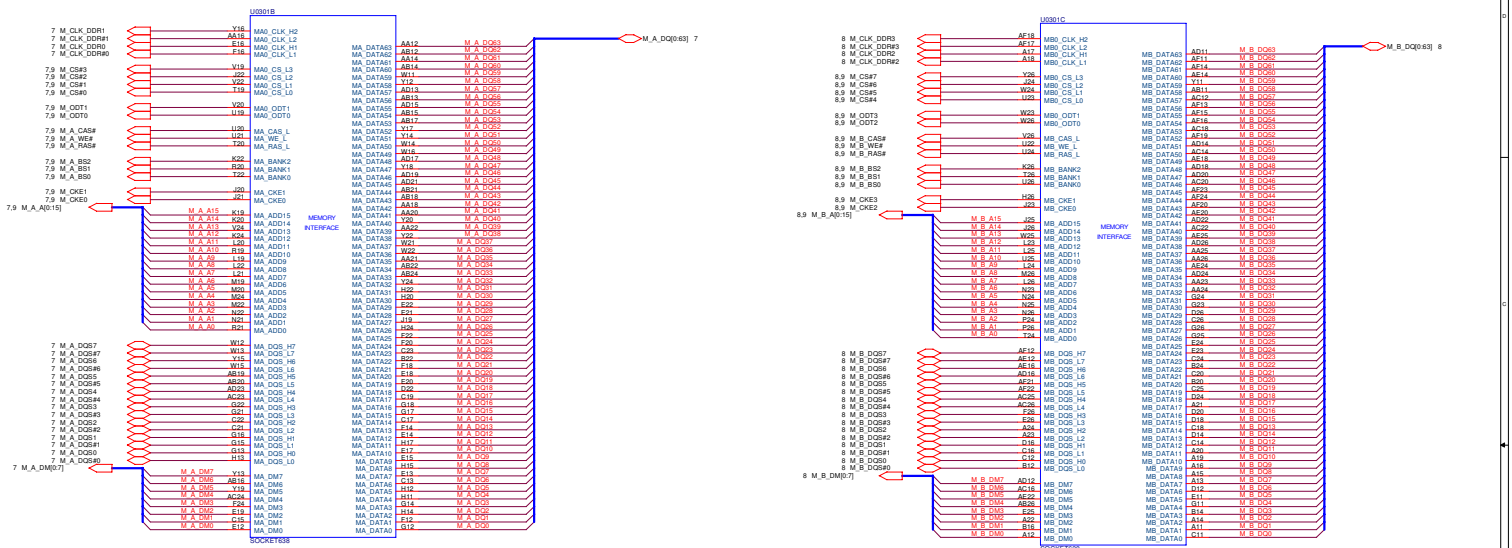








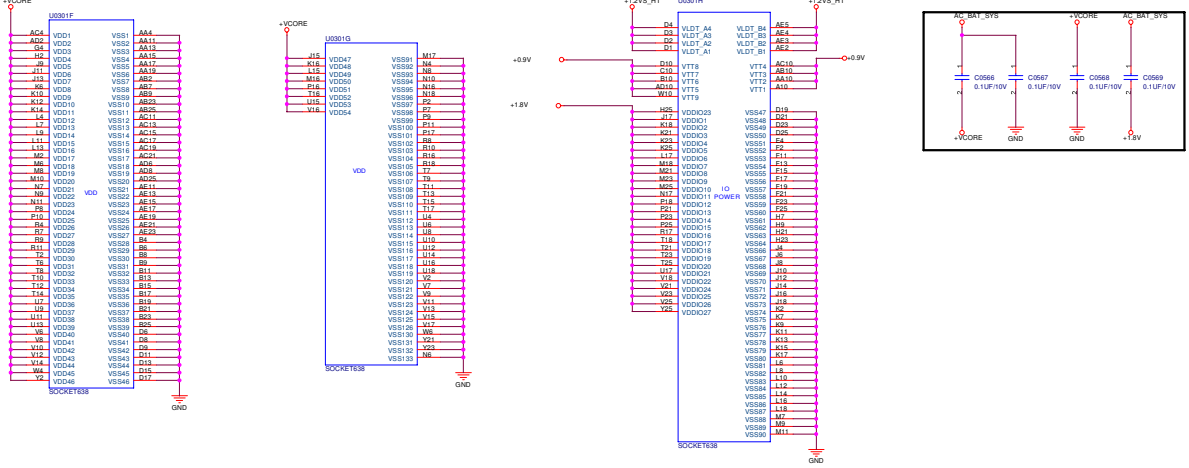


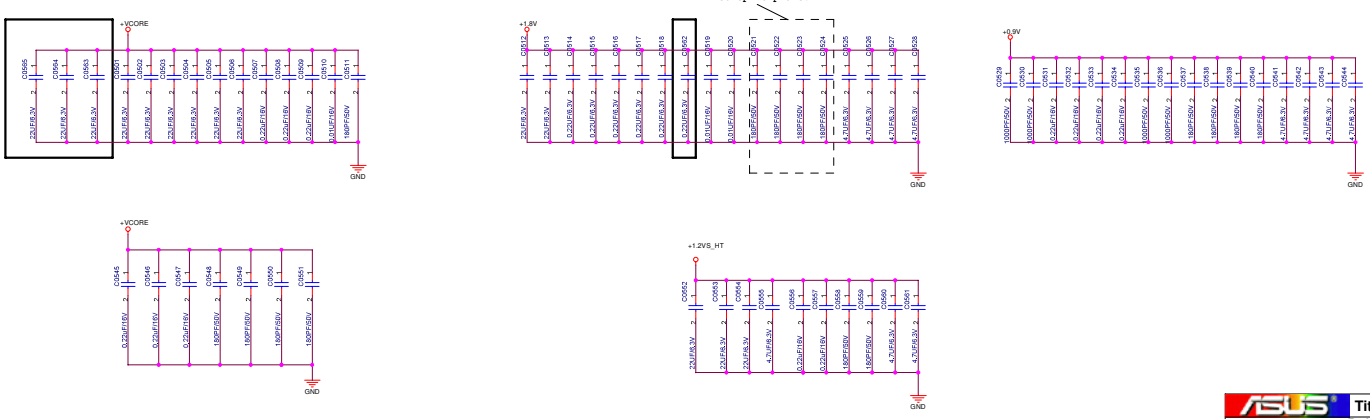


ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二 四月 09, 2007	Sheet 4 of 88		

35A

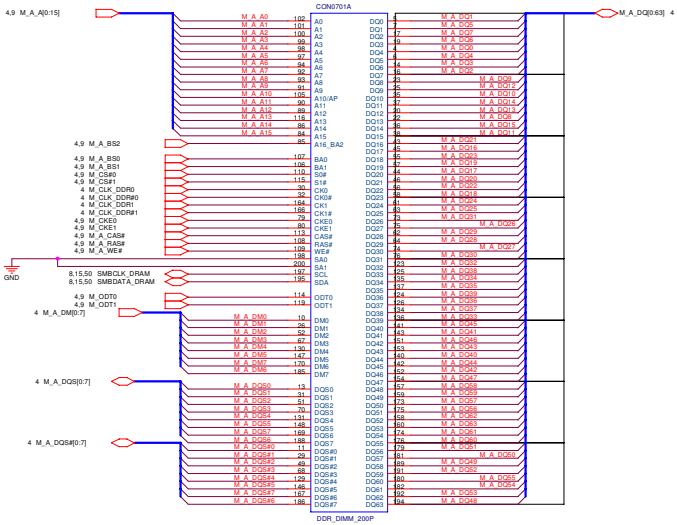
For EMI

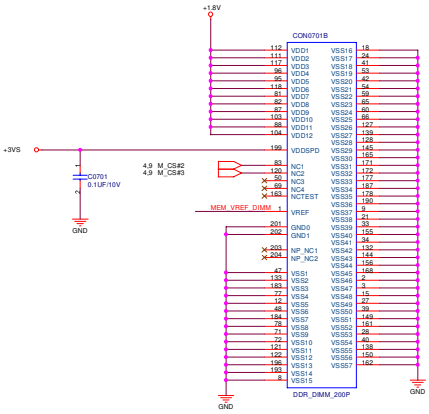


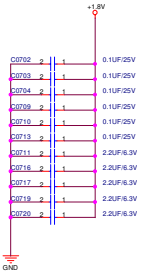


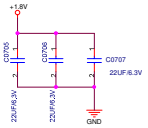
Title : F5U			
ASUSTeK Computer INC	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 5 of 88		

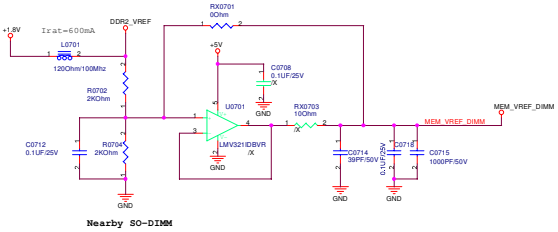
ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 6 of 88	



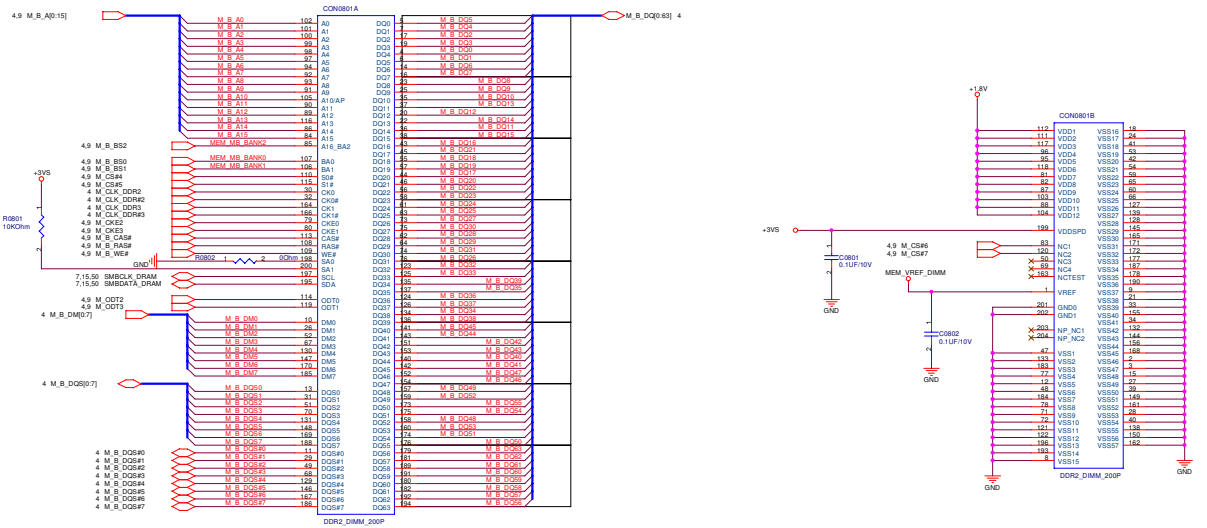




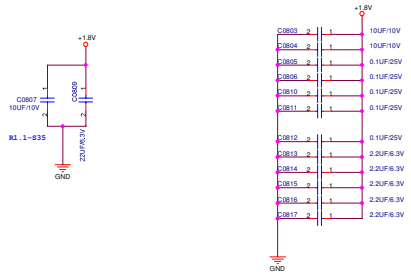




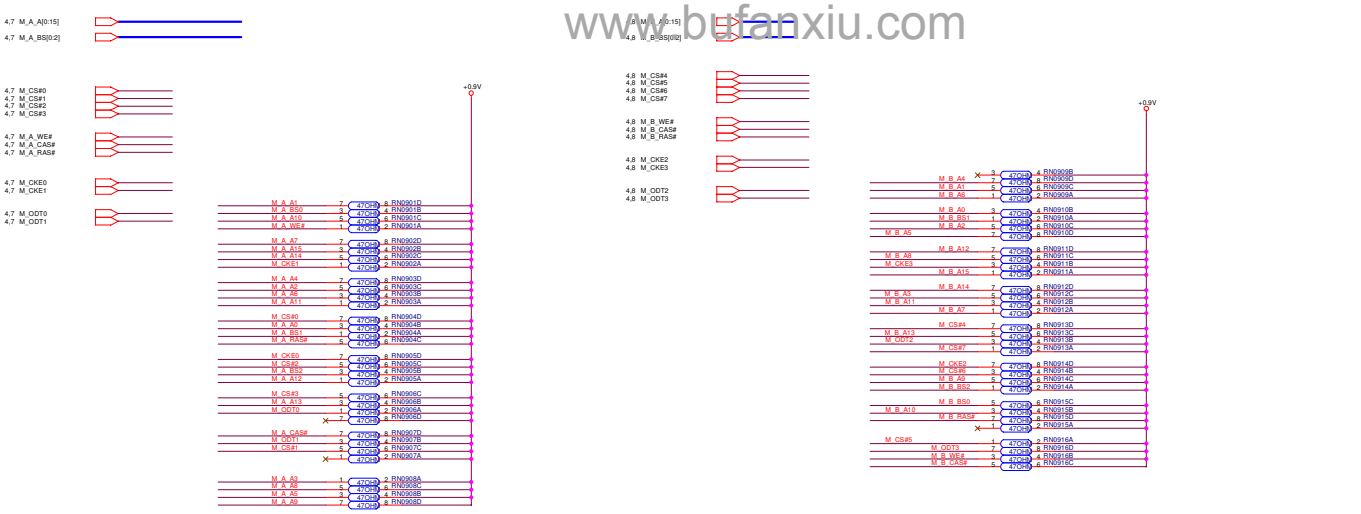
ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 7 of 88		



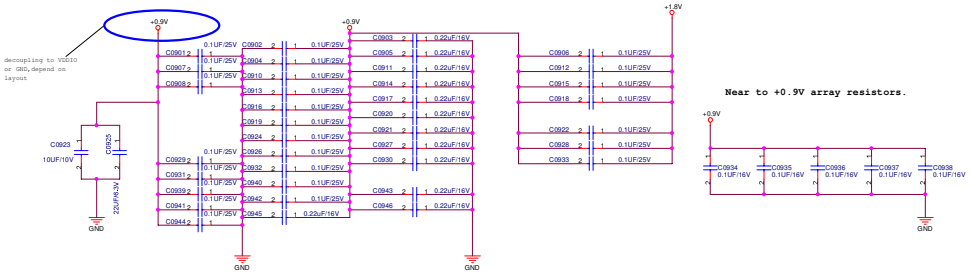
PN:12G025C22002



ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 8 of 88	

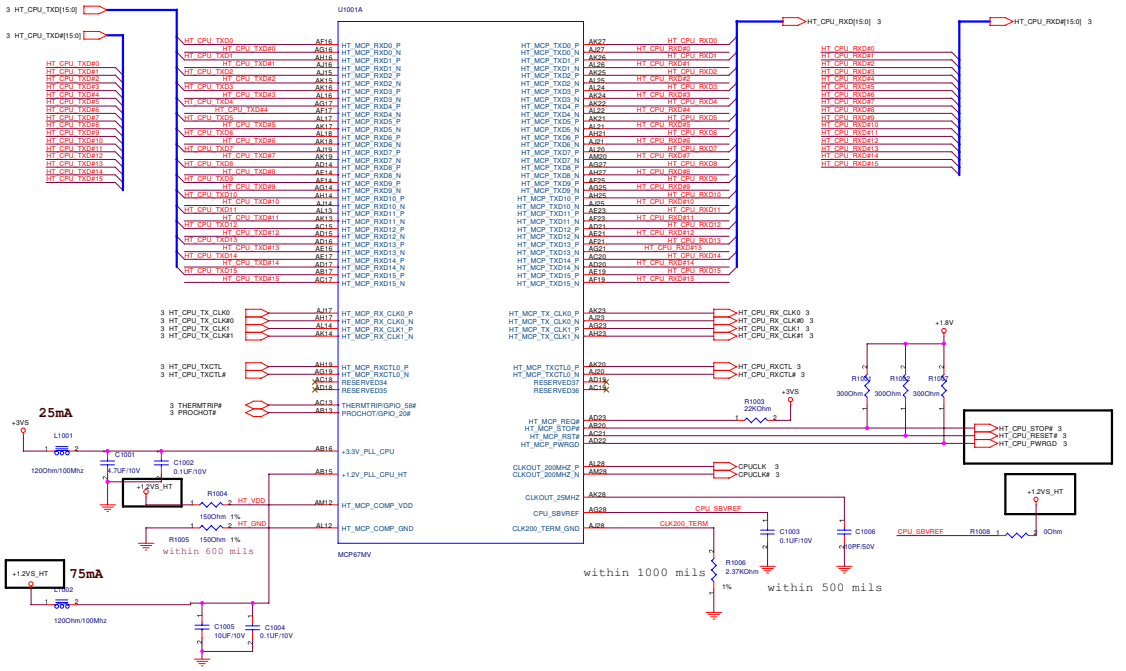


Layout Note: Place one cap close to every 2 pullup resitors terminated to +0.9V

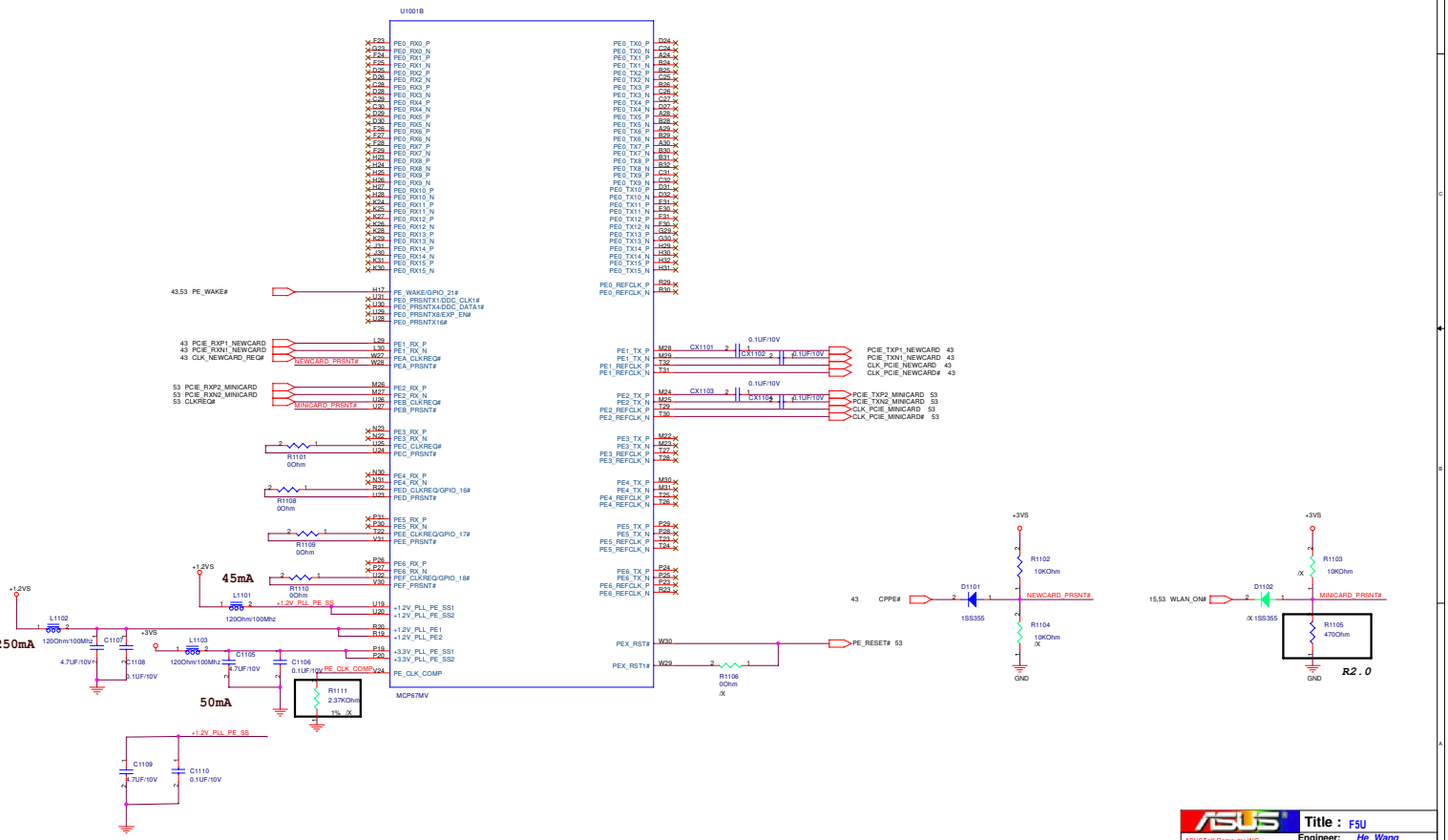


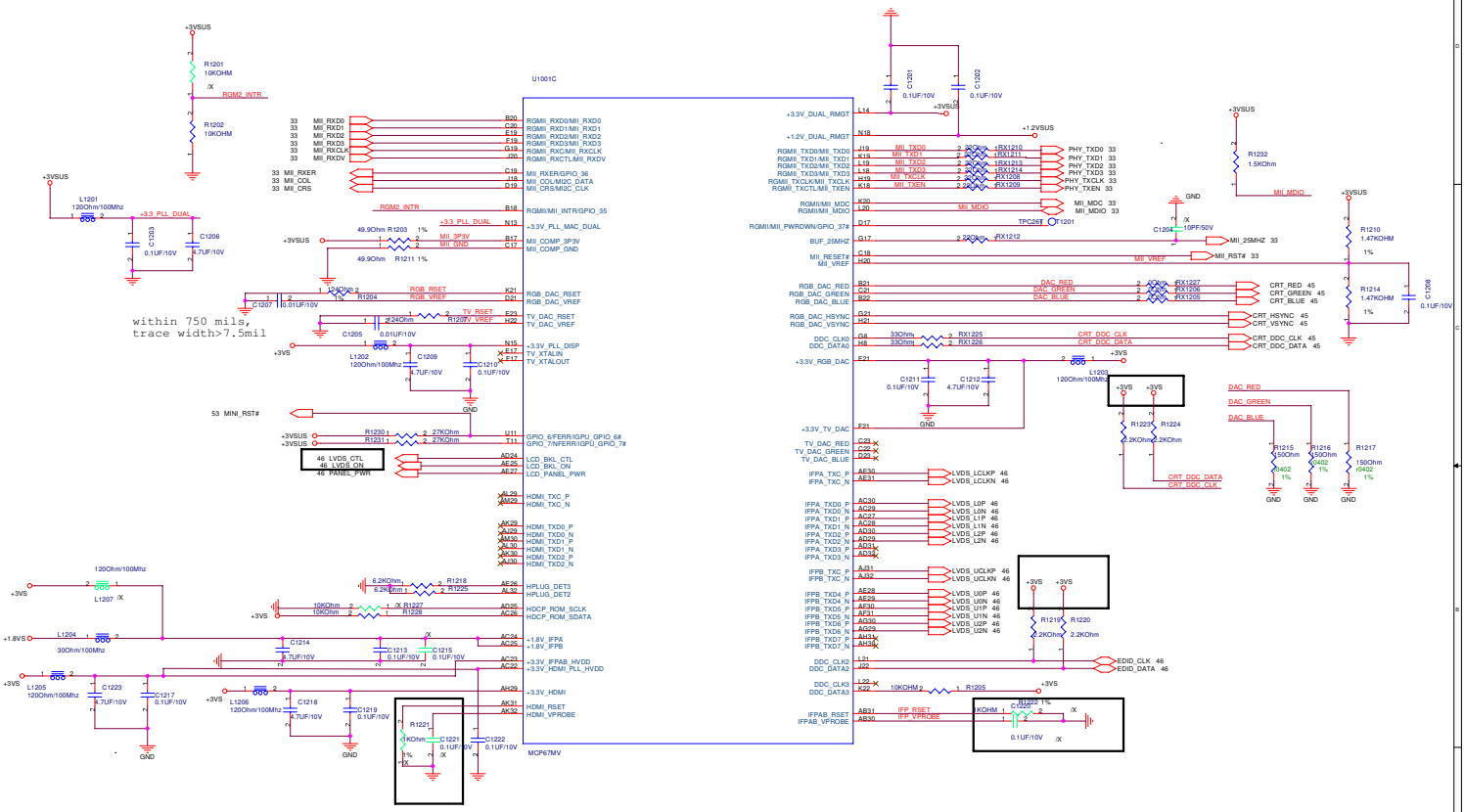
ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 9 of 88		

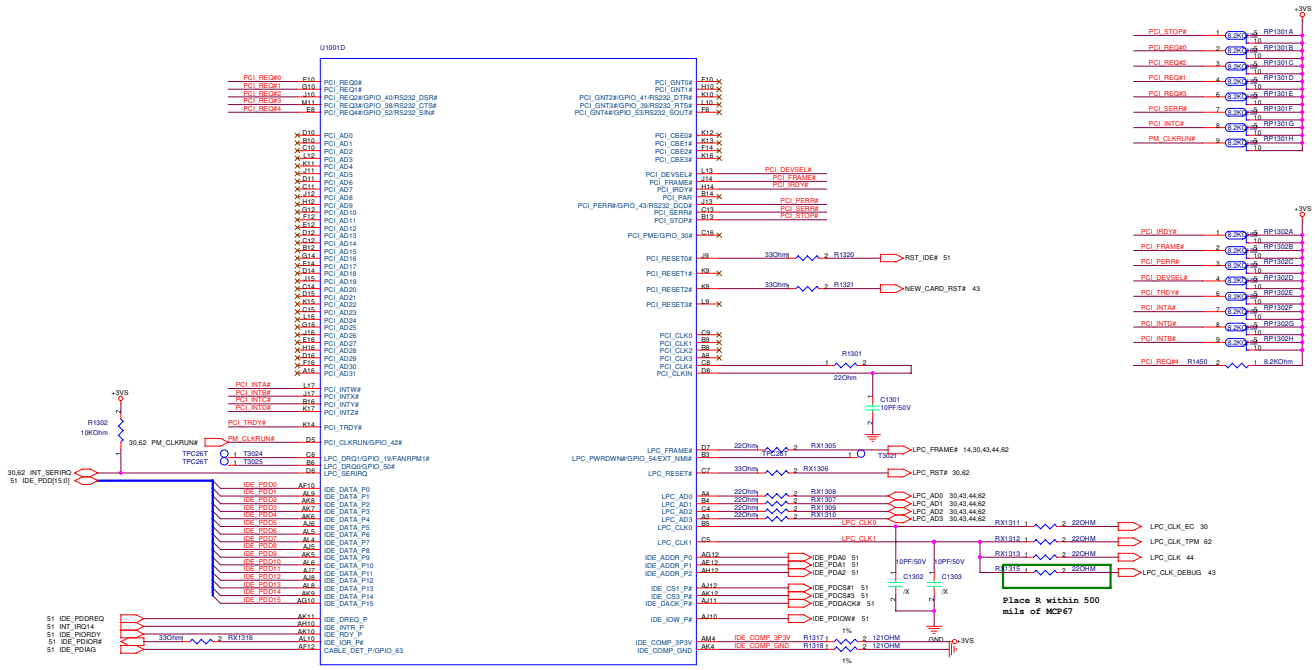
<< Kennedy_Zhang >>



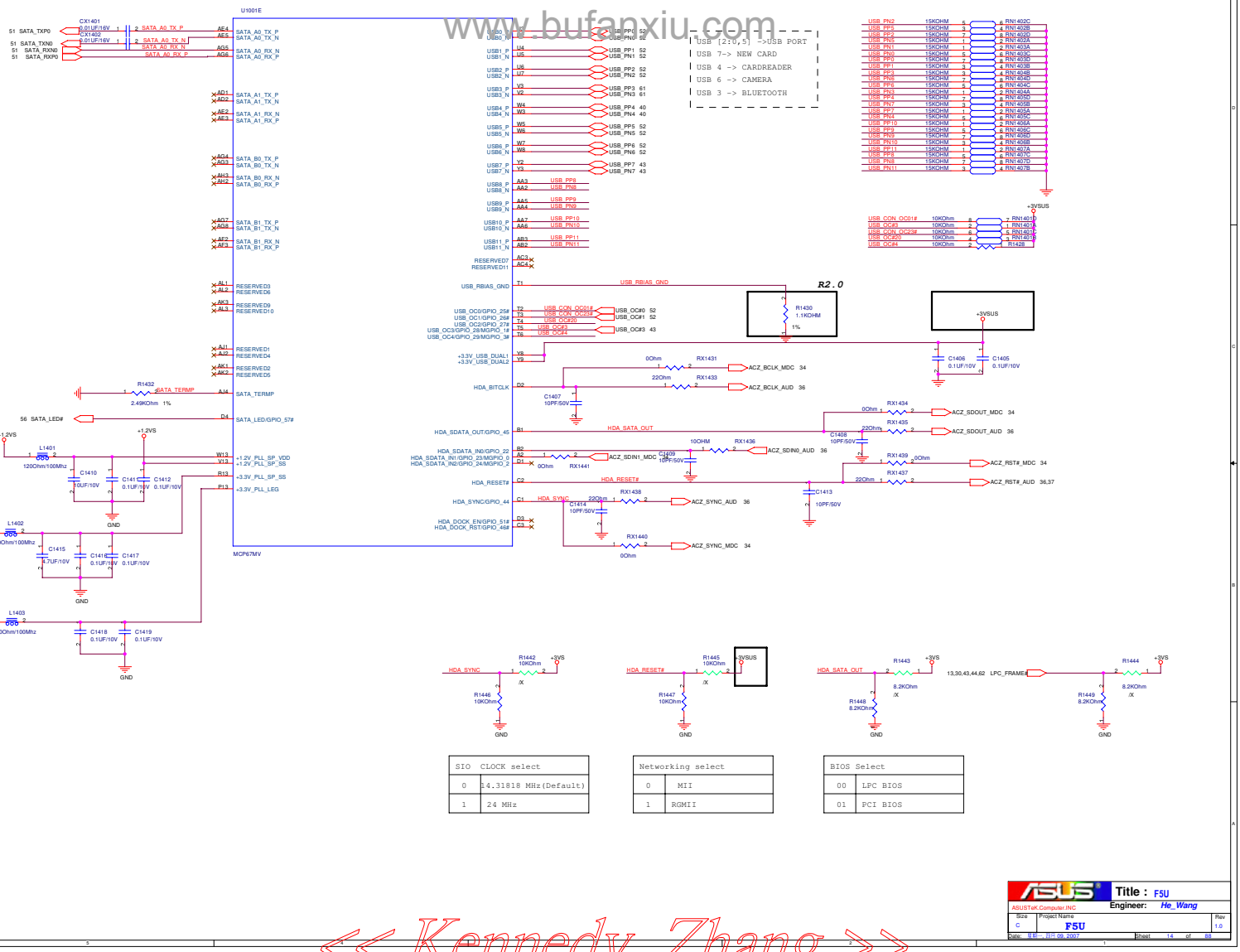
ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 10 of 88		







ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 13 of 88	



SIO CLOCK select	
0	14.31818 MHz(Default)
1	24 MHz

Networking select	
0	MII
1	RGMII

BIOS Select	
00	LPC BIOS
01	PCI BIOS

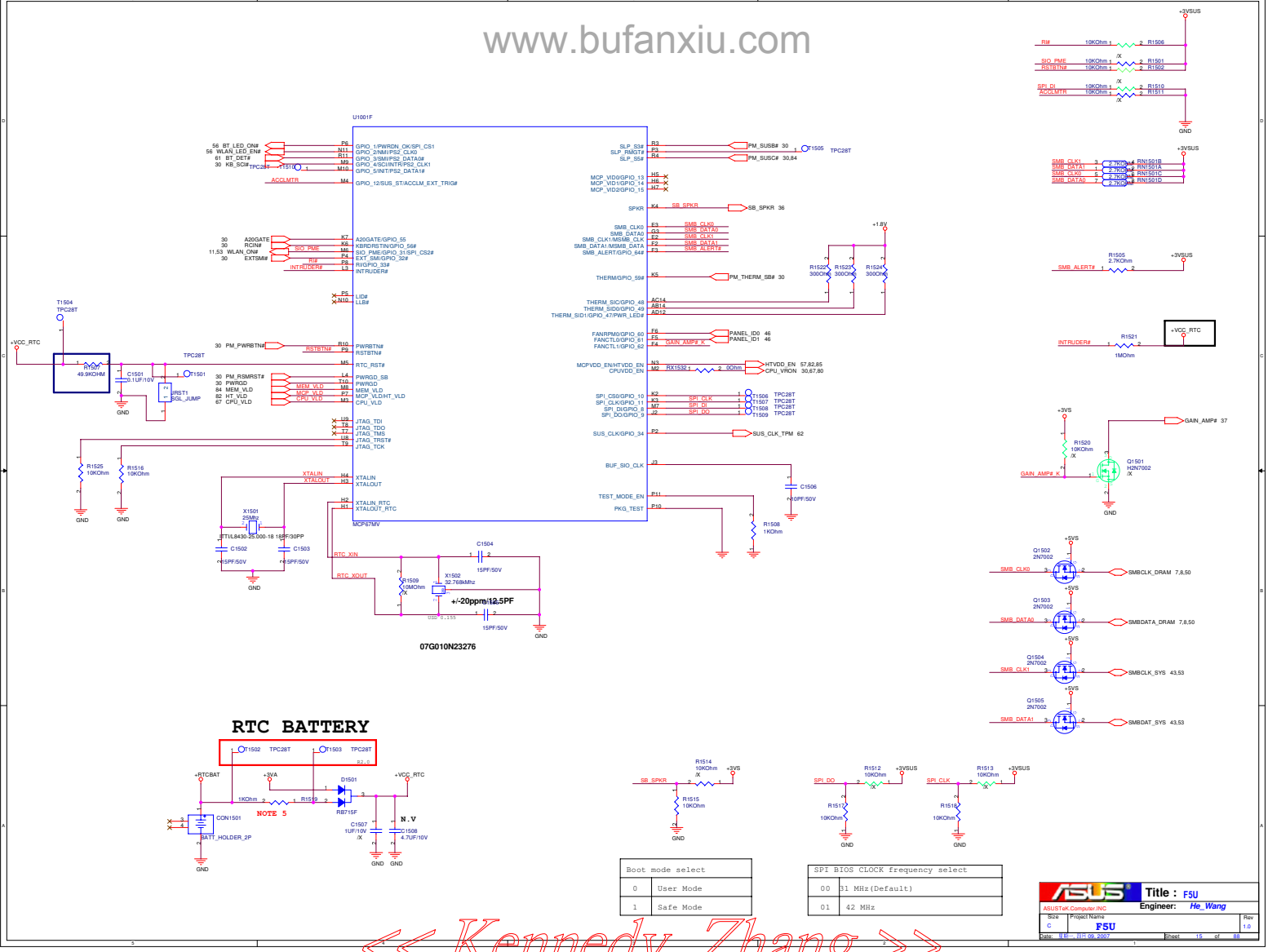
ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 四月 09, 2007 Sheet 14 of 88



RTC BATTERY

Boot mode select	
0	User Mode
1	Safe Mode

SPI BIOS CLOCK frequency select	
00	31 MHz(Default)
01	42 MHz

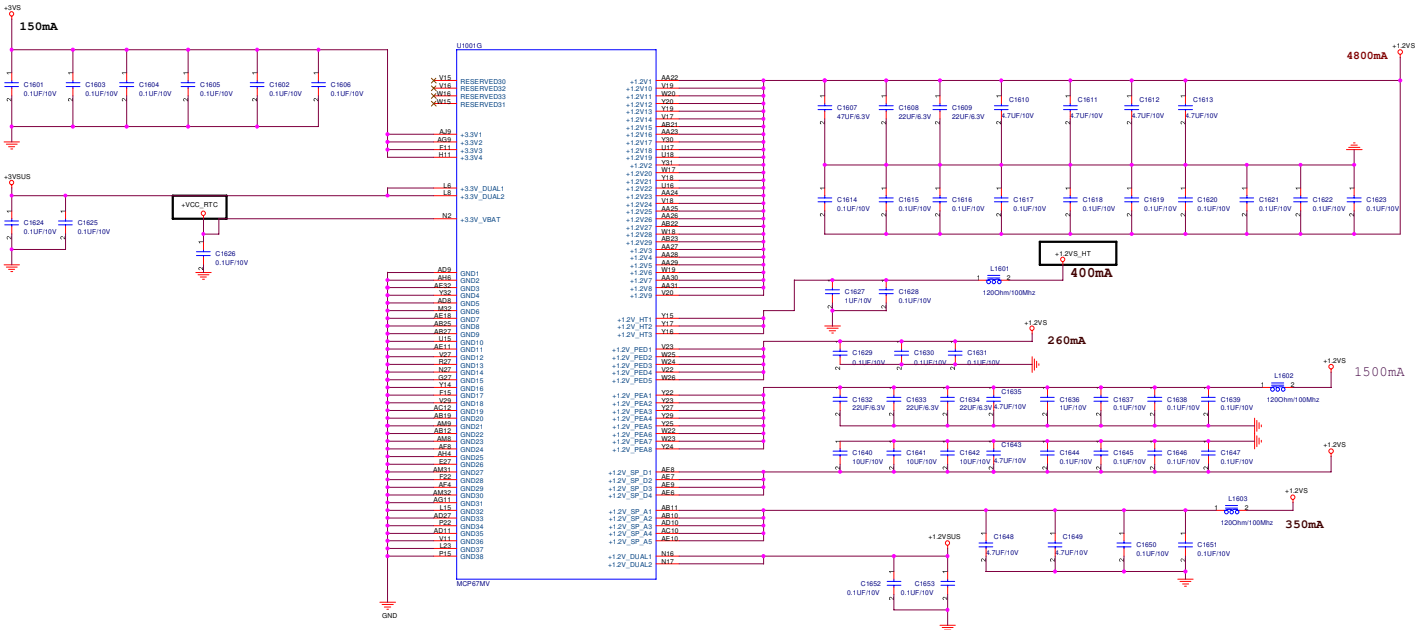
ASUSTeK Computer INC

Title : F5U

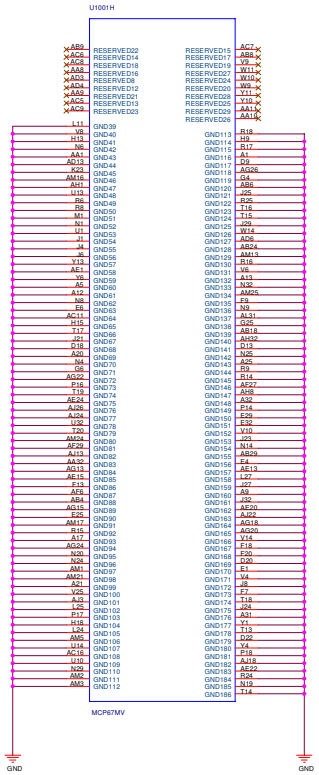
Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期二 四月 09, 2007 Sheet 15 of 88



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期三, 三月 28, 2007	Sheet 18 of 88		



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期三, 三月 28, 2007	Sheet 17 of 88		

ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 18 of 88		

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期三, 十月 28, 2007 Sheet 19 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 20 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 21 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 22 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 23 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 24 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 25 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 28 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 27 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 28 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 29 of 88

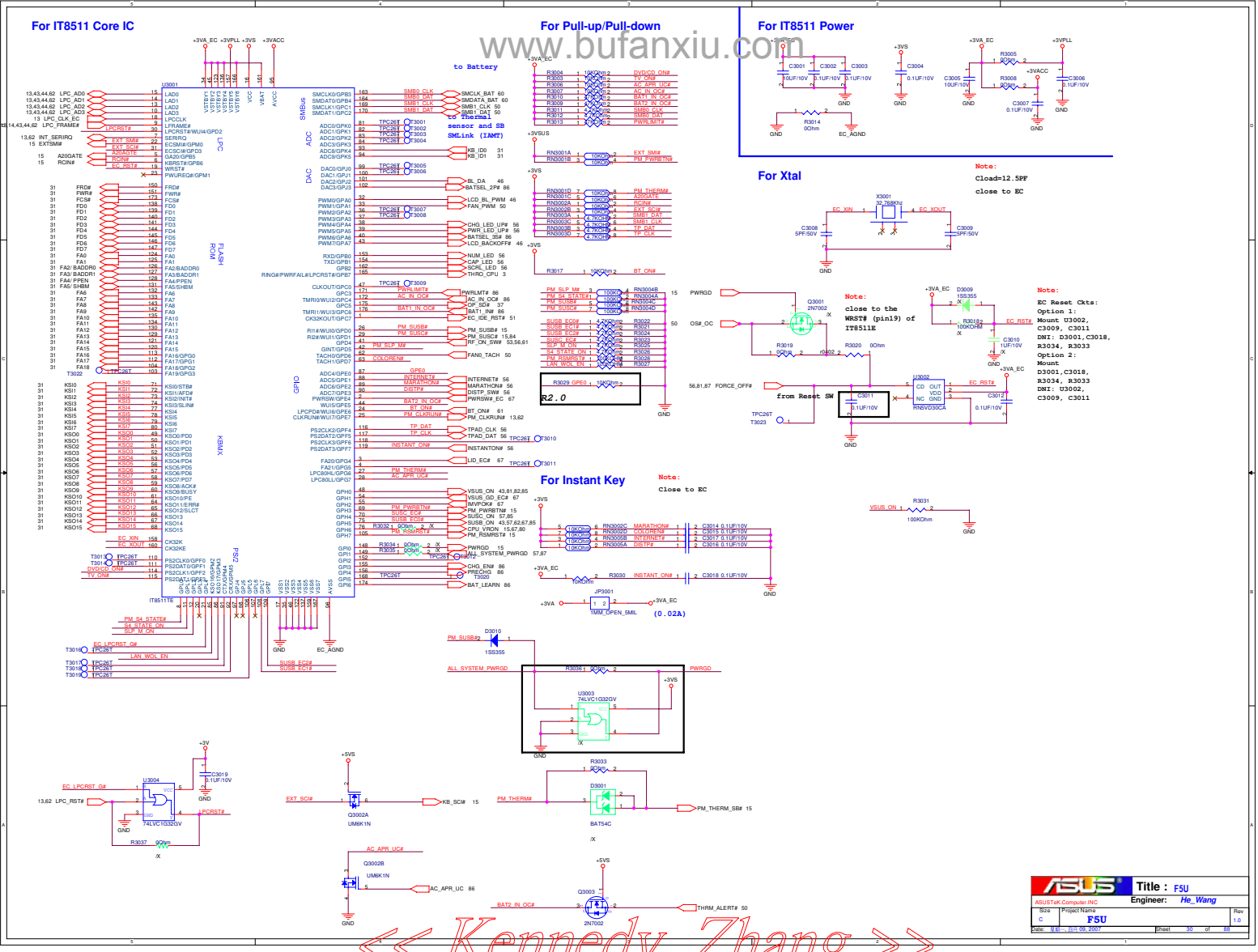
For IT8511 Core IC

For Pull-up/Pull-down

For IT8511 Power

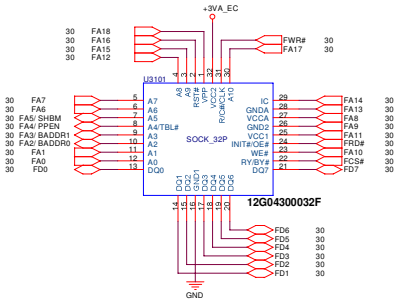
For Xtal

For Instant Key



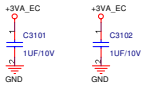
ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U	Rev: 1.0	
Date: 日曜, 日月 09, 2007	Sheet 30 of 88		

ISA ROM
(4Mbits)

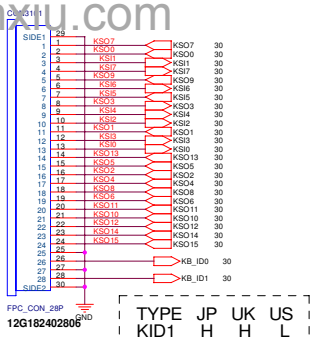


SST-PLCC32 4Mbits Flash ROM
PN:05G001014110
(FLASH SST SST39VF040-70-4C-NHE
4M-70 PLCC-32)

If need to use 8Mbits ISA ROM, could choose SST39LF080.
But it does not have PLCC32 package.
ASUS does not have part number yet.



KBC connector



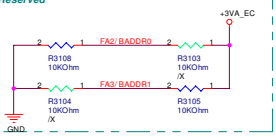
TYPE	JP	UK	US
KID1	H	H	L
KID2	L	H	L

ISA ROM

EC Hardware Strapping

FA2/ BADDR0 & FA3/ BADDR1

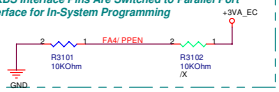
00: PNPCNG Access Register Pair Are 002Eh and 002Fh
10: PNPCNG Access Register Pair Are 004Eh and 004Fh
01: PNPCNG Access Register Pair Are Determined by EC Domain Registers SWCBALR and SWCBAHR.
11: Reserved



Note: Sampled at VSTBY Power Up Reset

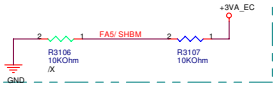
FA4/ PPEN

0: Normal
1: KBS Interface Pins Are Switched to Parallel Port Interface for In-System Programming



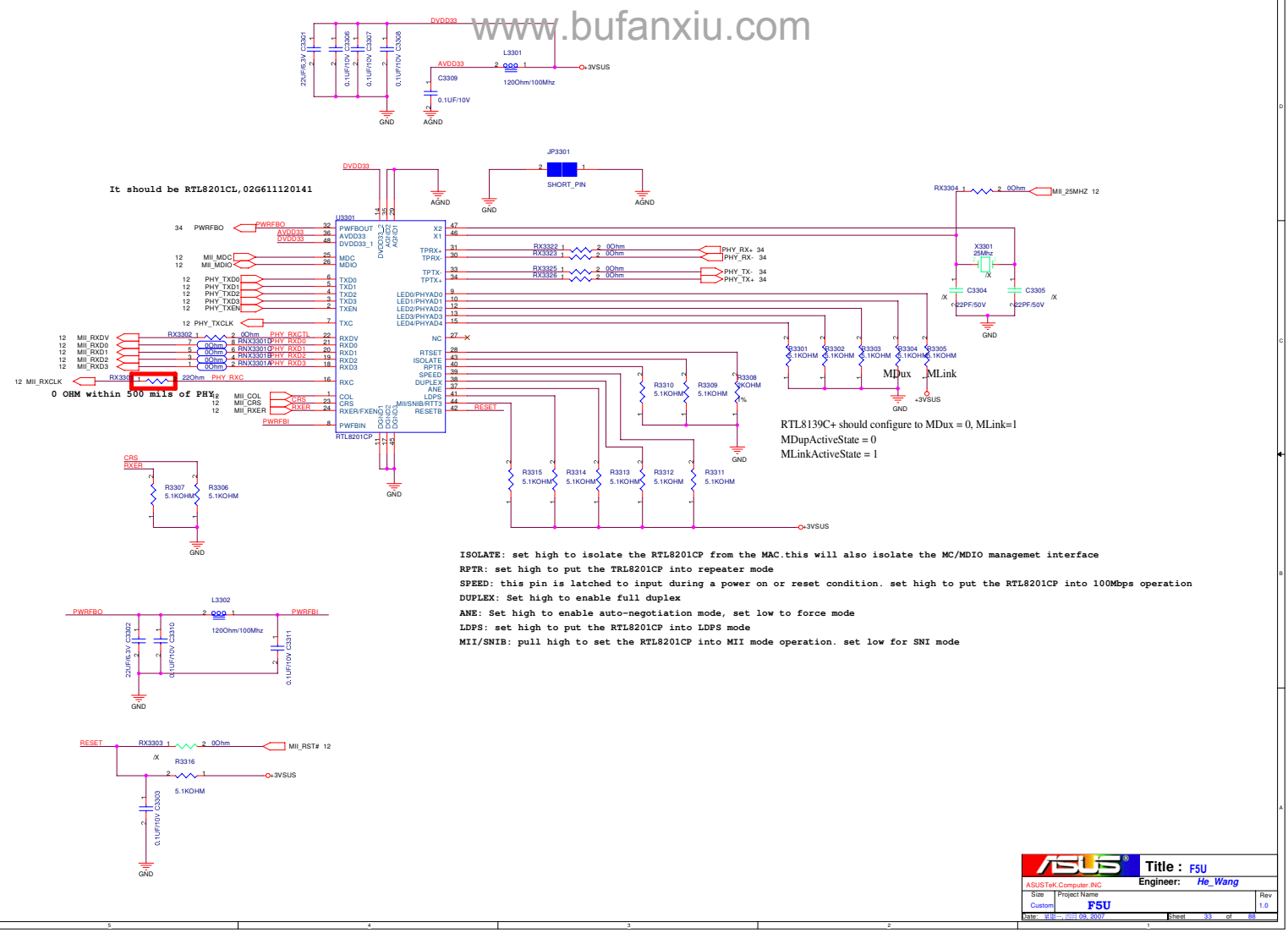
FA5/ SHBM

0: Disable Shared Memory with Host BIOS
1: Enable Shared Memory with Host BIOS



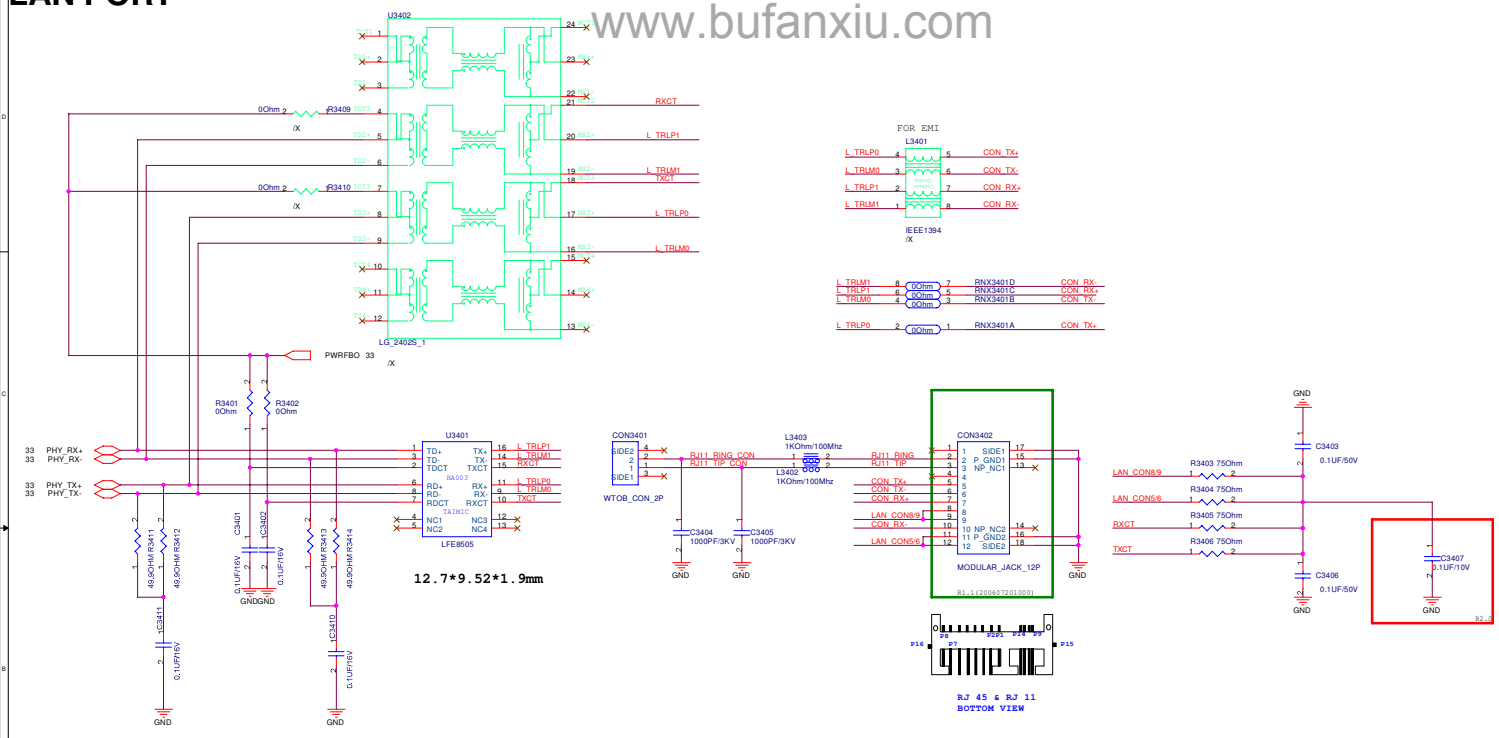
ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 五月 09, 2007	Sheet 31 of 88		

ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 32 of 88	

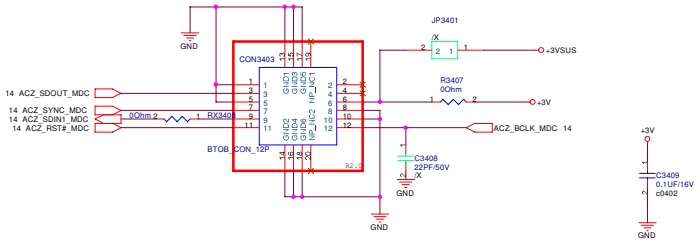


ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 试用一, 四月 09, 2007	Sheet 33 of 88		

LAN PORT



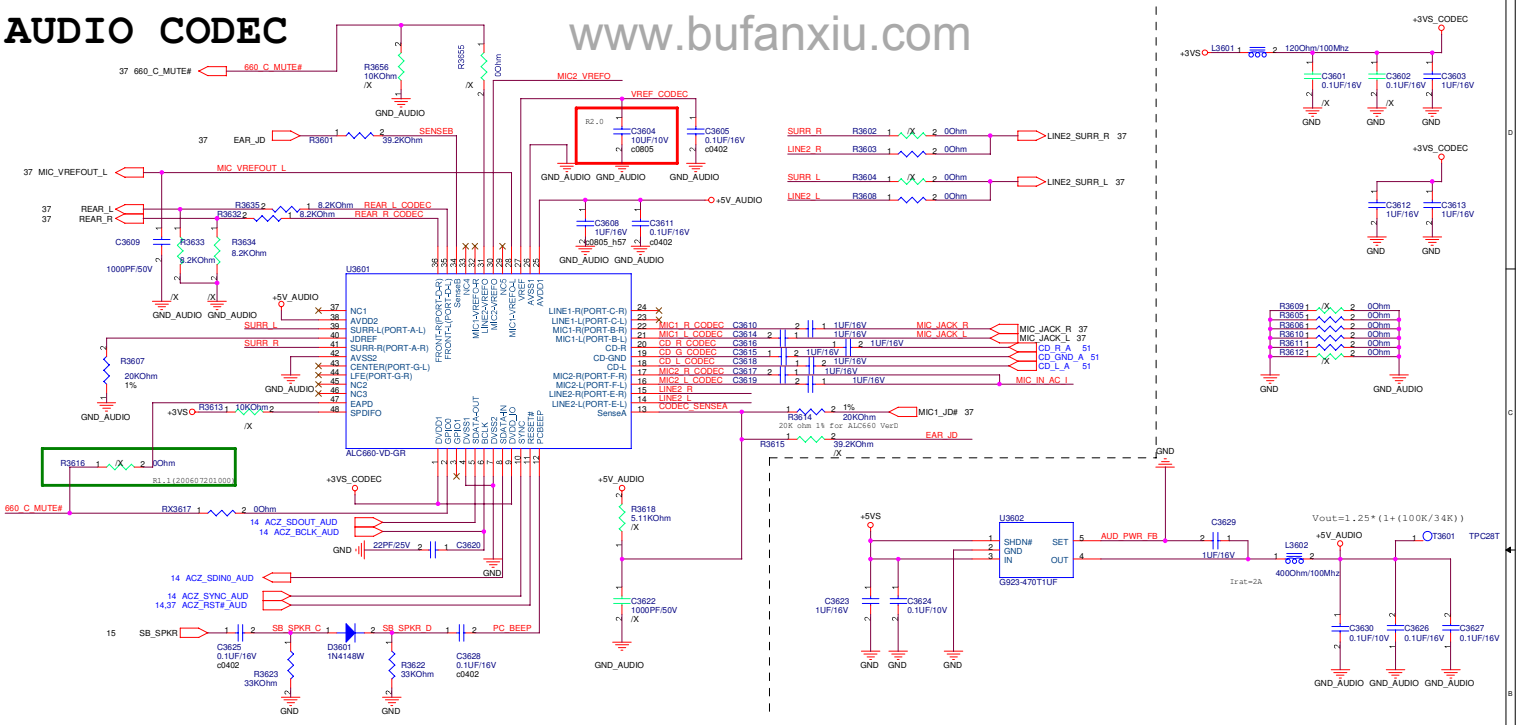
MDC CONN.



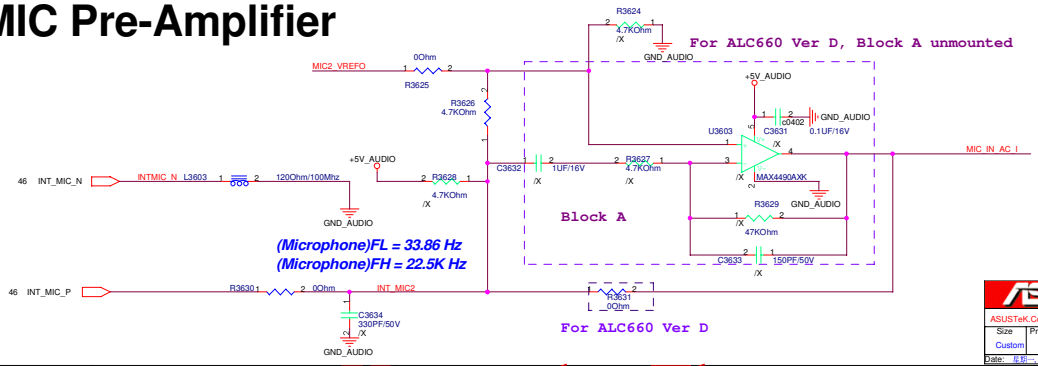
ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 试用一, 四月 09, 2007	Sheet 34 of 88		

ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 星期一, 一月 29, 2007		Sheet 35 of 88	

AUDIO CODEC

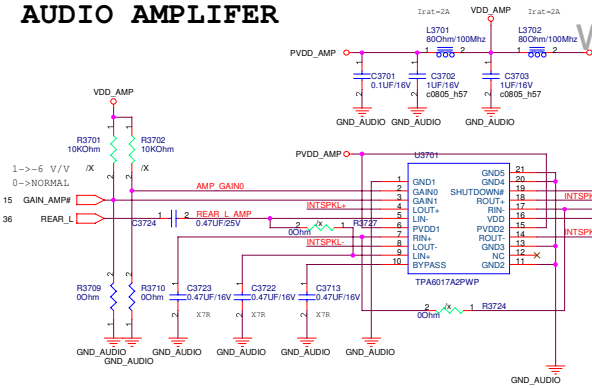


Internal MIC Pre-Amplifier



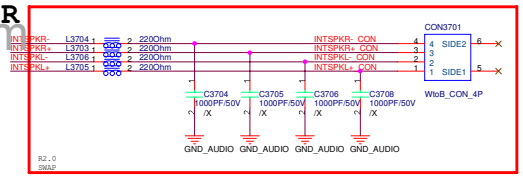
ASUSTeK.Computer.INC		Title :	F5U
		Engineer:	He_Wang
Size	Project Name		Rev
Custom	F5U		1.0
Date: 星期一, 四月 09, 2007		Sheet 36 of 88	

AUDIO AMPLIFER

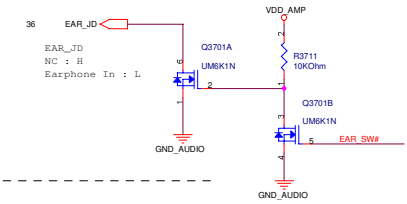


SPEAKER

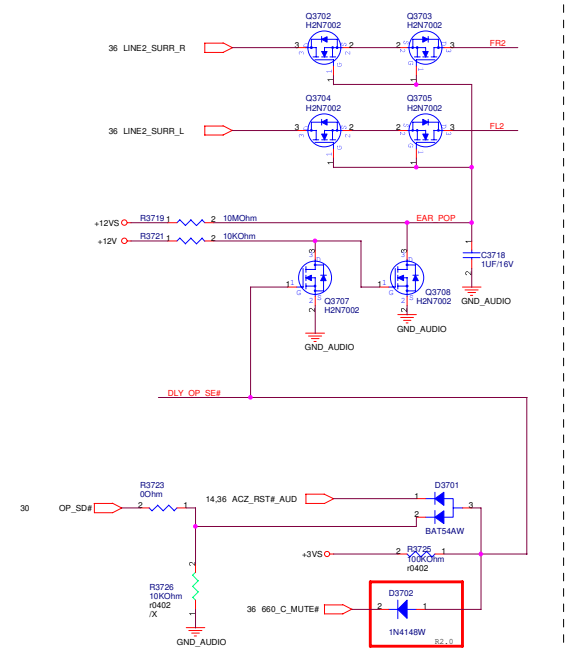
To Internal Speaker Connector

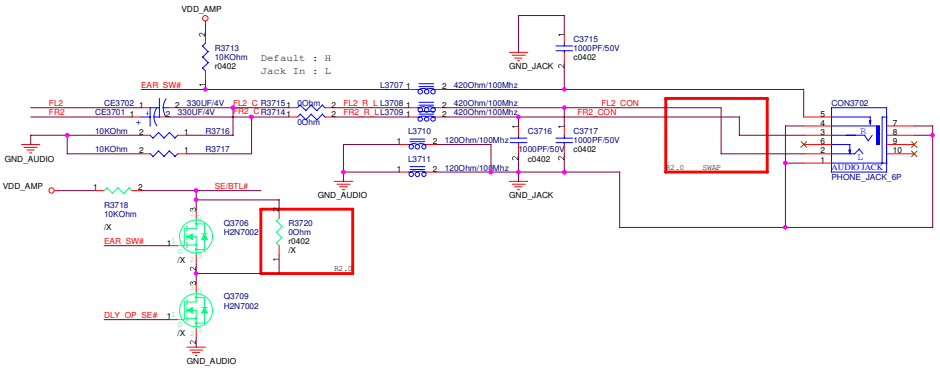


HeadPhone

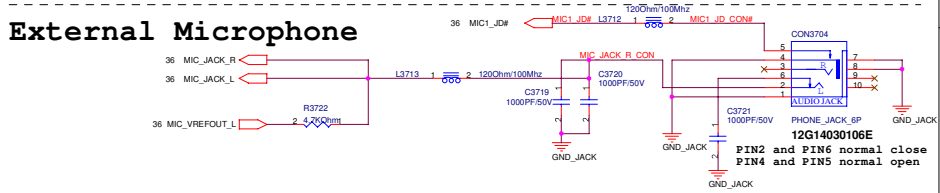


DEPOP CIRCUIT





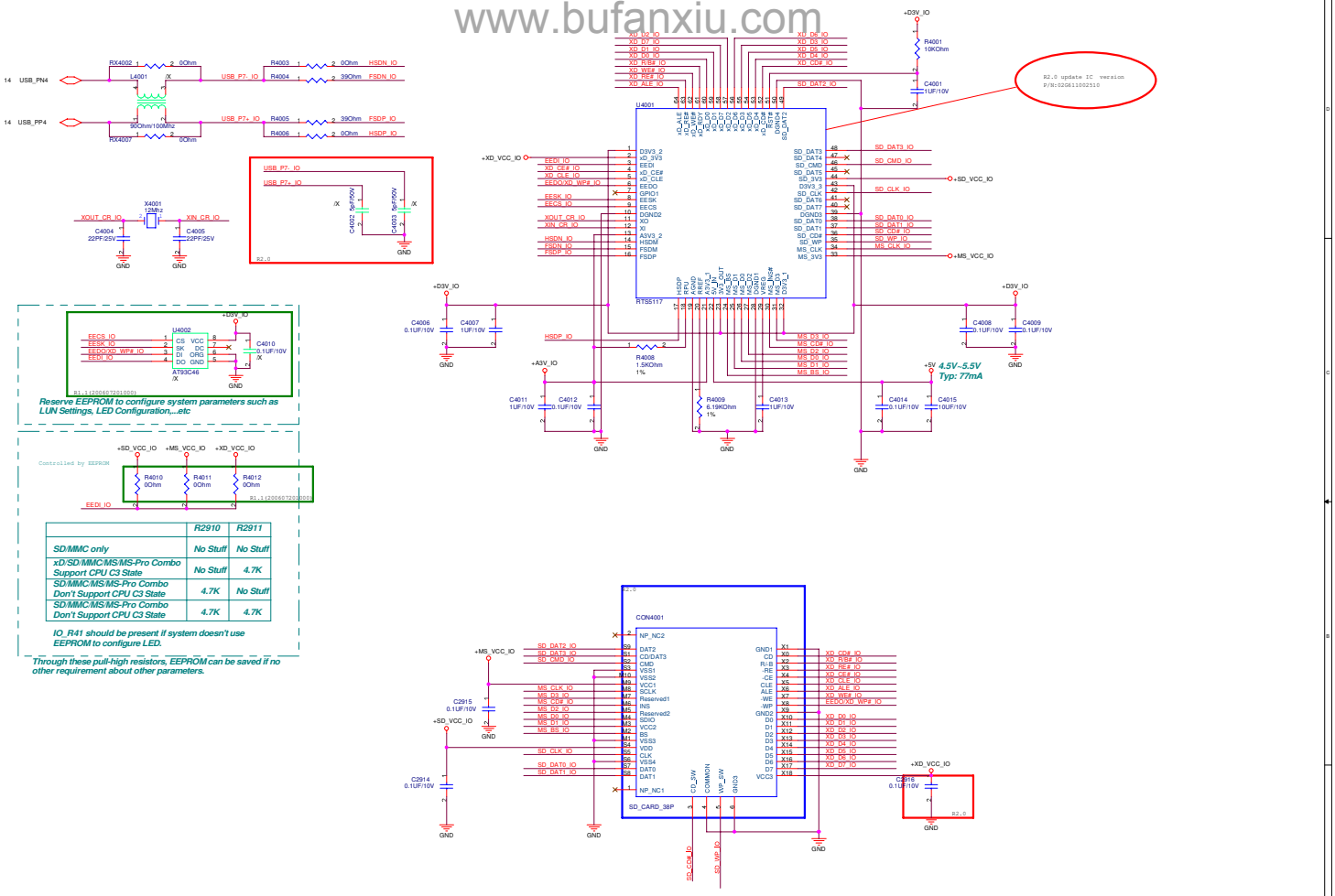
External Microphone



ASUSTeK.Computer.INC	Title : F5U	
	Engineer: He_Wang	
Size: Custom	Project Name: F5U	Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 37 of 88	

ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 38 of 88		

ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 39 of 88	



	R2910	R2911
SD/MMC only	No Stuff	No Stuff
xD/SD/MMC/MS/MS-Pro Combo Support CPU C3 State	No Stuff	4.7K
SD/MMC/MS/MS-Pro Combo Don't Support CPU C3 State	4.7K	No Stuff
SD/MMC/MS/MS-Pro Combo Don't Support CPU C3 State	4.7K	4.7K

IO_R41 should be present if system doesn't use EEPROM to configure LED.

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期二, 四月 09, 2007 Sheet 40 of 88

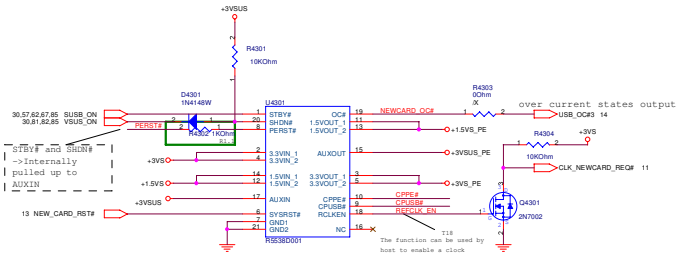
<< Kennedy_Zhang >>

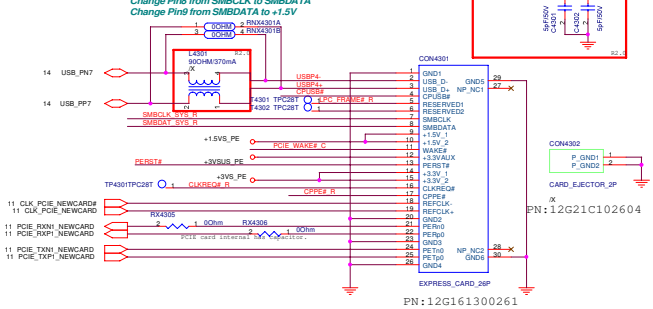
ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 星期三, 三月 28, 2007	Sheet 41 of 88		

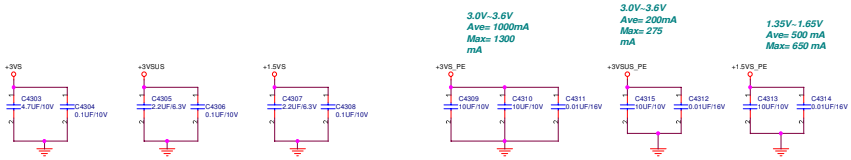
Title : F5U			
ASUSTeK.Computer.INC	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 星期一, 一月 28, 2007	Sheet 42 of 88		

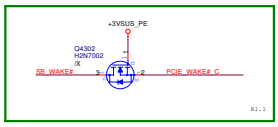
NEW CARD SOCKET

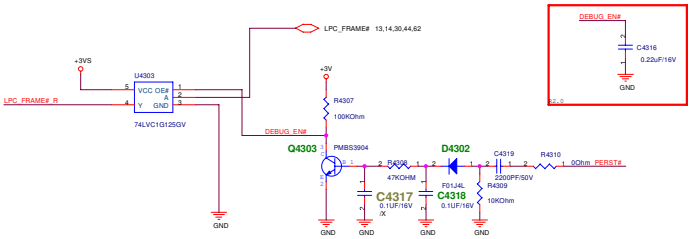
!! ExpressCard Standard 1.0:
Change Pin7 from RESERVED to SMBCLK
Change Pin8 from SMBCLK to SMBDATA
Change Pin9 from SMBDATA to +1.5V

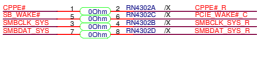


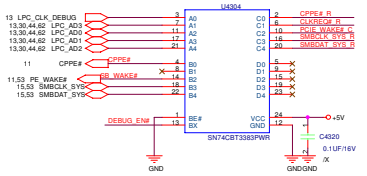






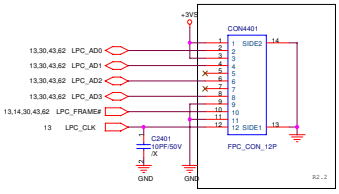






ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 43 of 88		

LPC DEBUG CONNCTOR



ASUSTeK Computer INC

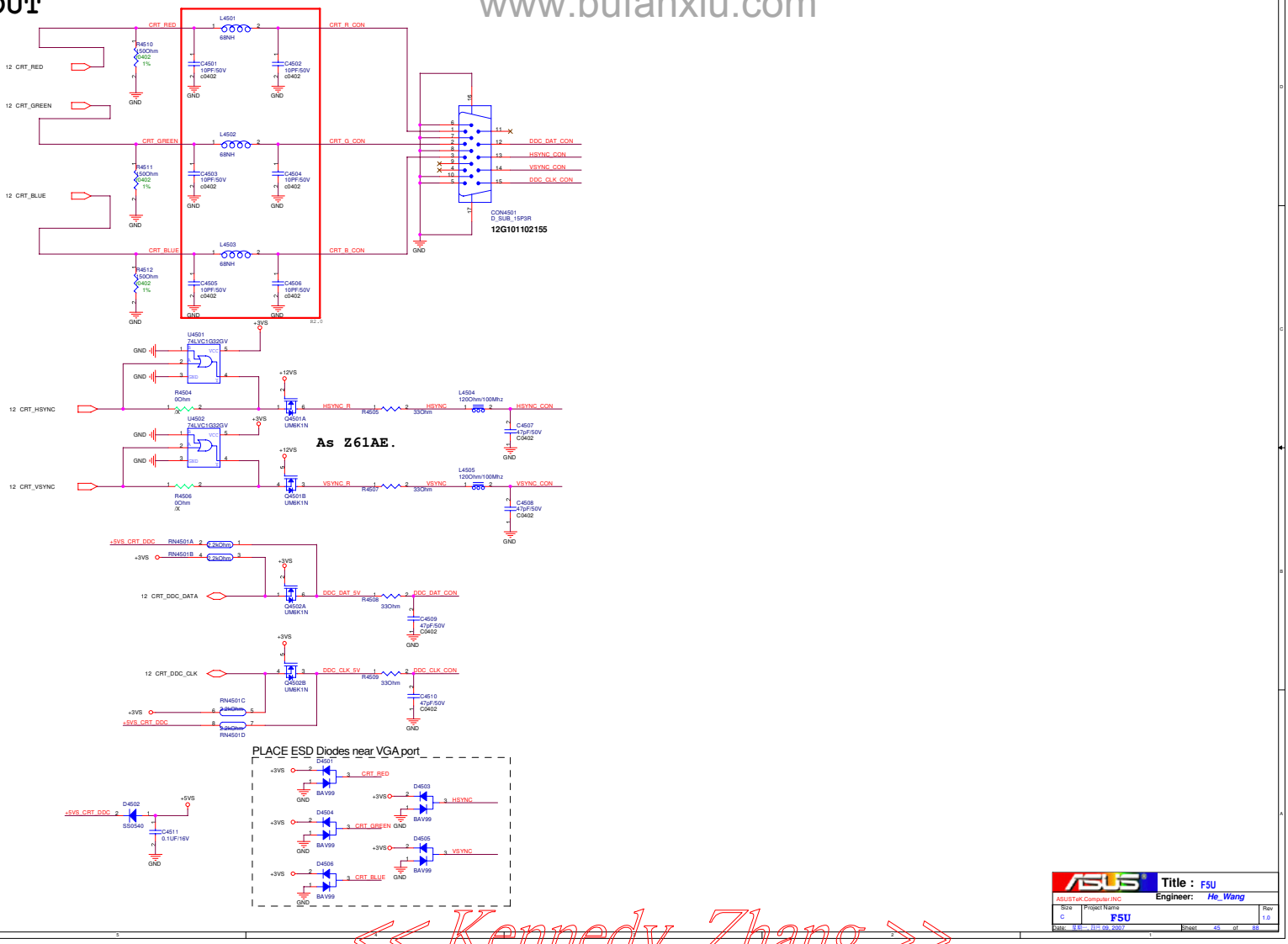
Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

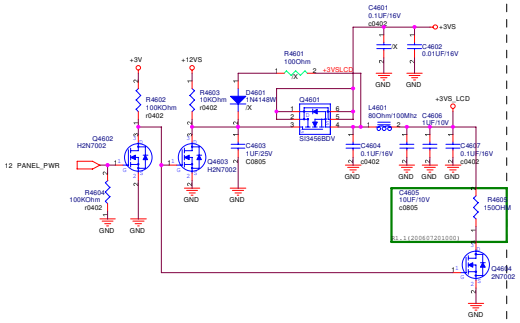
Date: 星期二, 四月 09, 2007 Sheet 44 of 88

VGA OUT

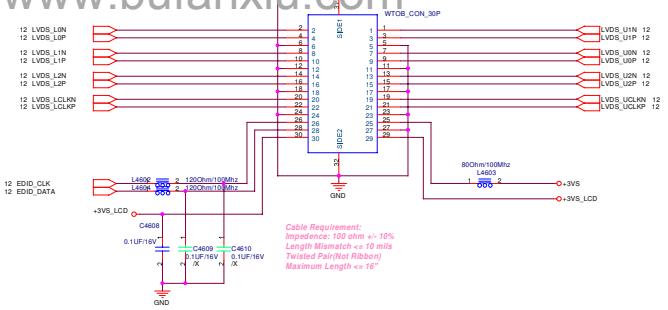


ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 45 of 88	

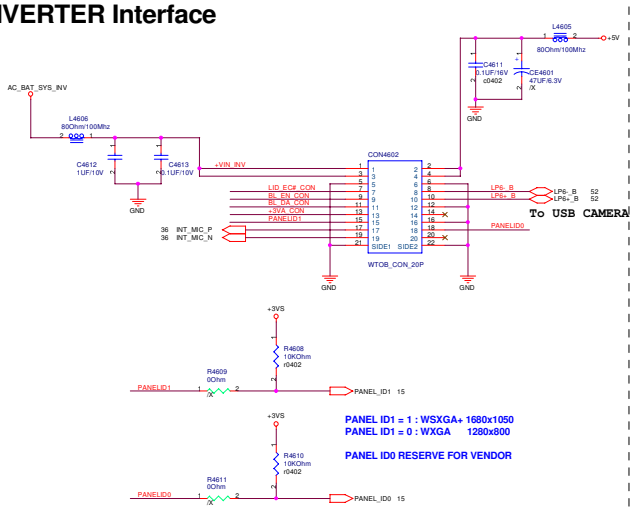
LCD Power



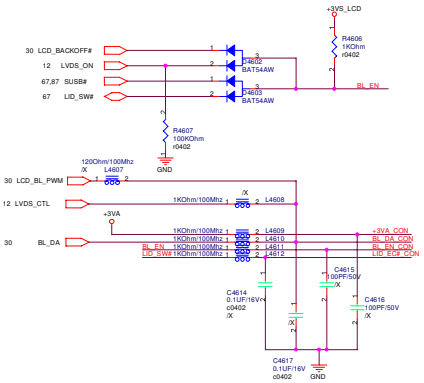
LCD LVDS Interface



INVERTER Interface



Backlight Enable



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 46 of 88		

ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: Custom	Project Name: F5U		Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 47 of 88		

ASUSTeK Computer INC

Title : F5U

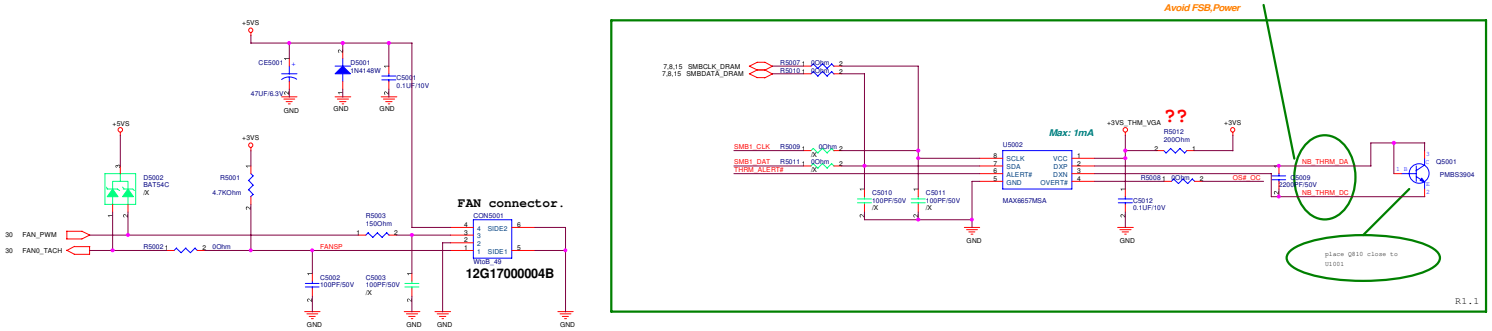
Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

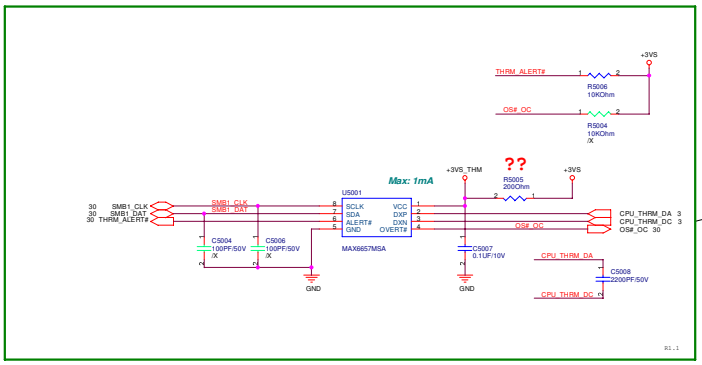
Date: 星期三, 十月 28, 2007 Sheet 48 of 88

ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 49 of 88	

DC FAN control



Thermal Sensor



Route CPU_THRM_DA and CPU_THRM_DC on the same layer

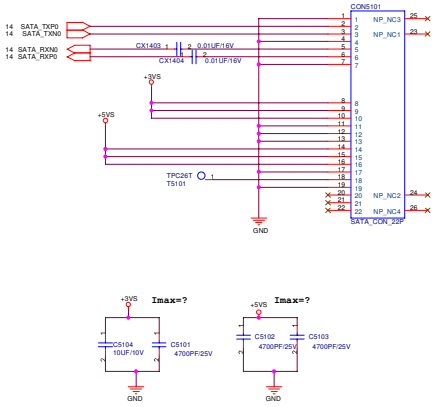
----------OTHER SIGNALS
15 mils
==========GND
10 mils
==========H_THERMDA(10 mils)
10 mils
==========H_THERMDC(10 mils)
10 mils
==========GND
15 mils
----------OTHER SIGNALS

Avoid FSB,Power

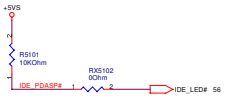
ASUSTeK.Computer.INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 50 of 66		

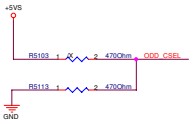
<< Kennedy_Zhang >>

SATA_HDD(Default)

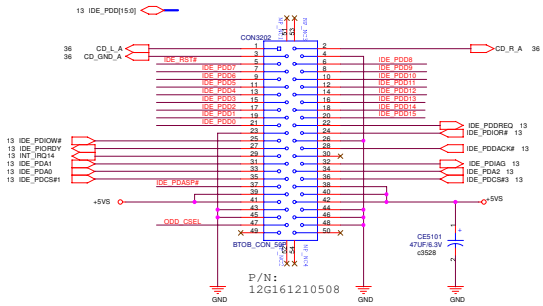


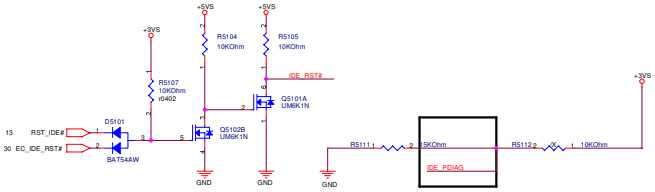
CD-ROM

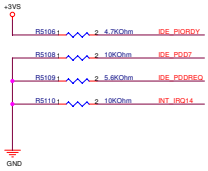




ODD_CSEL
Pull-Up: CDROM as Slave
Pull-Down: CDROM as Master







P/N: 12G161210508

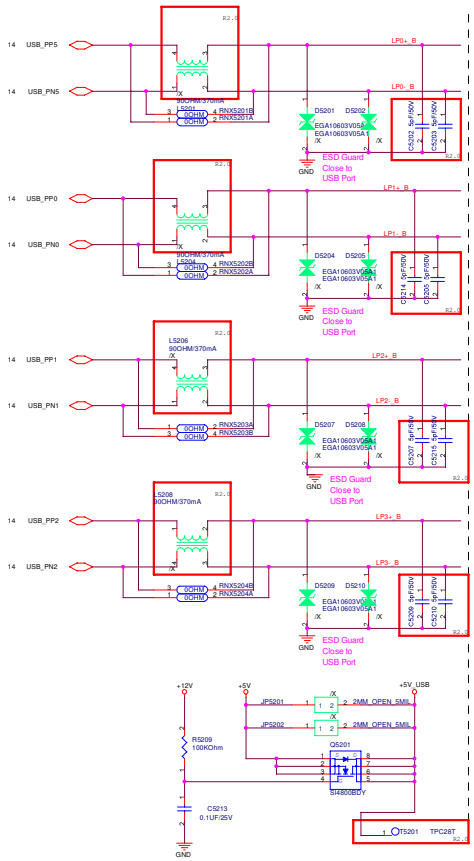
ASUSTeK Computer INC

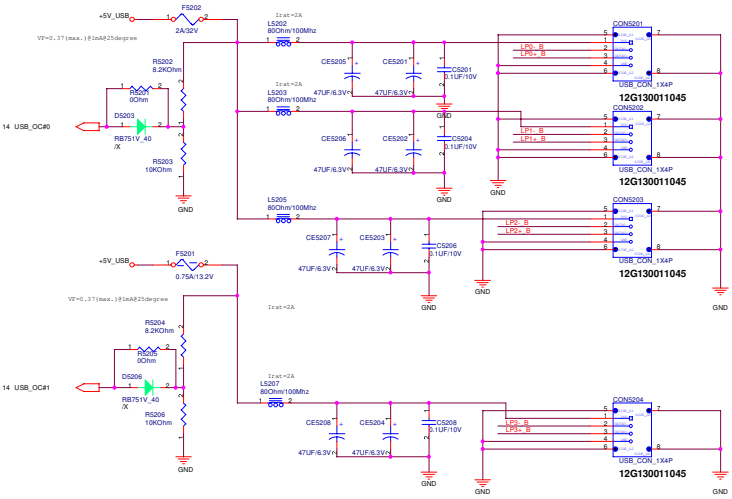
Title : F5U

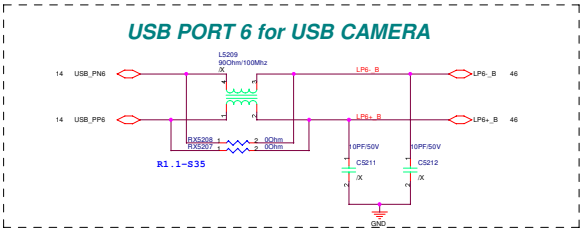
Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期二, 四月 09, 2007 Sheet 51 of 88



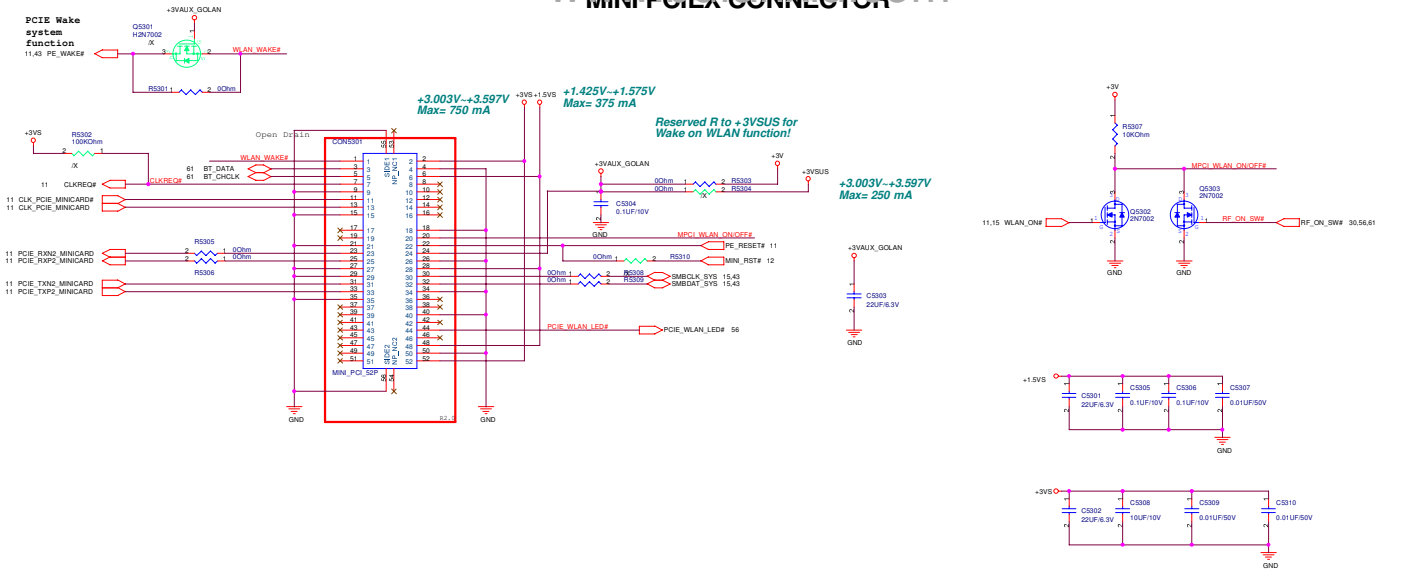




ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 52 of 88		

<< Kennedy_Zhang >>

MINI PCIEX CONNECTOR



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 53 of 88		

ASUSTeK Computer INC

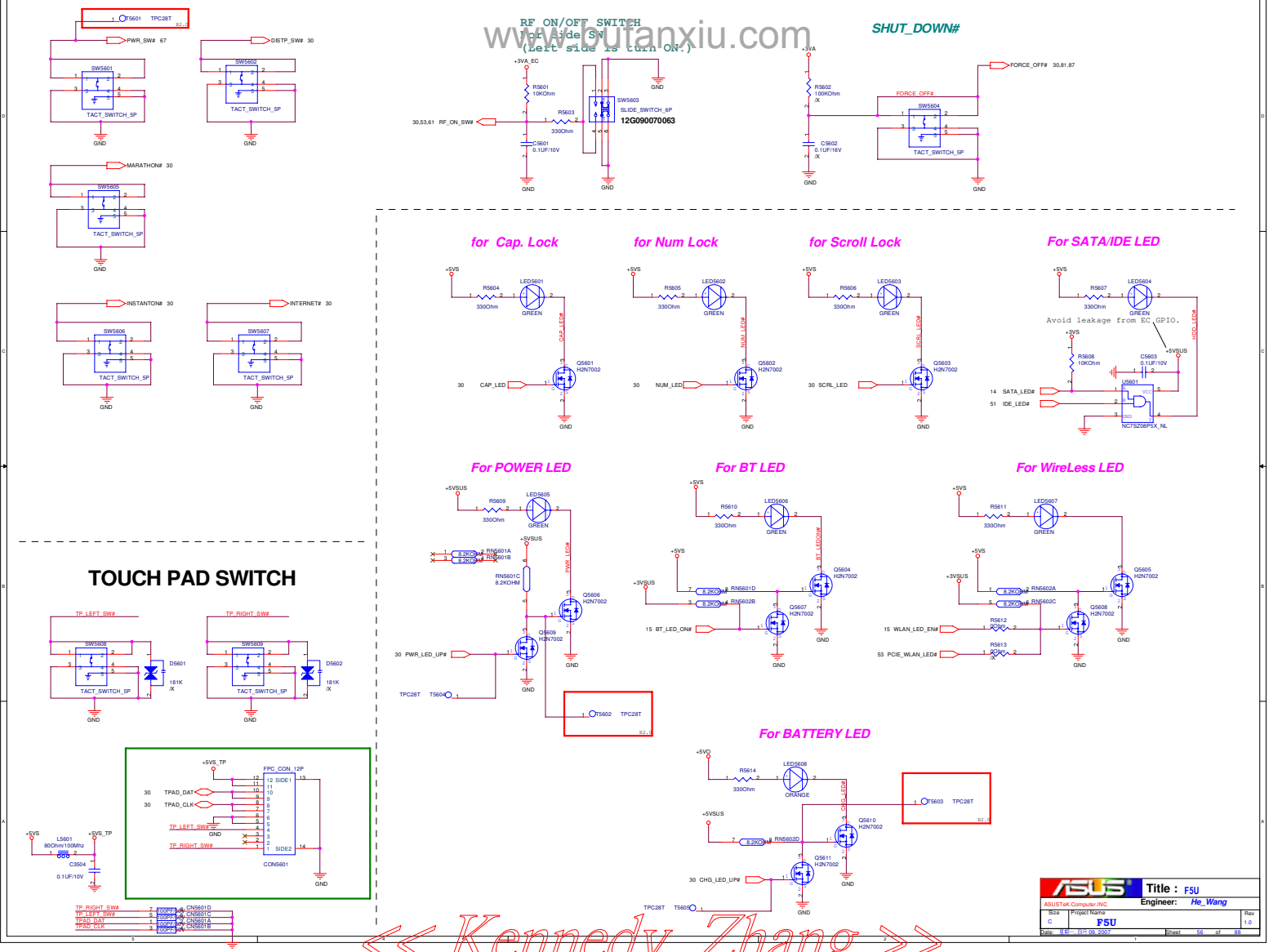
Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

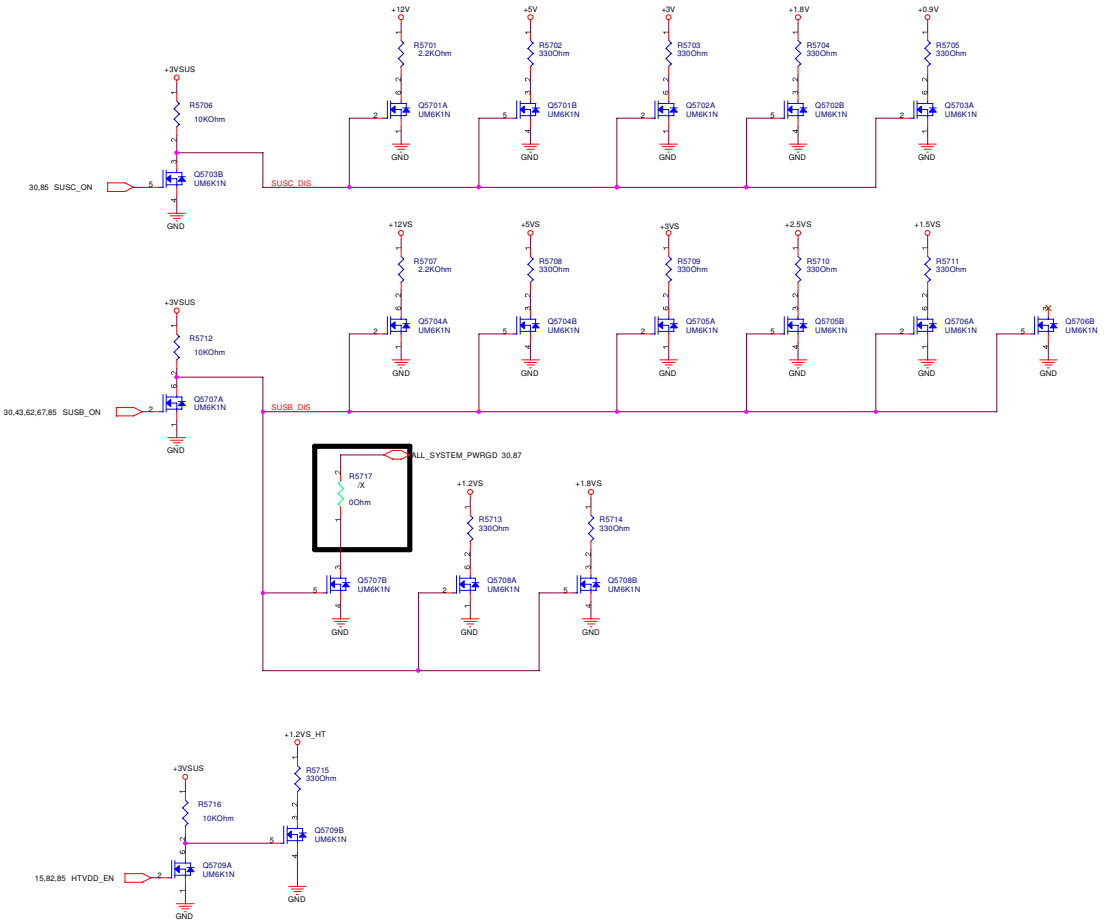
Date: 星期三, 十月 28, 2007 Sheet 54 of 88

ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 55 of 88	

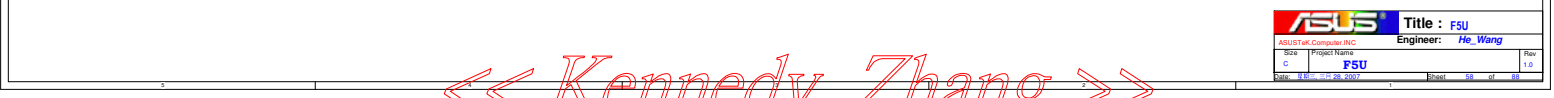


ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 56 of 88		

DISCHARGE CIRCUIT



ASUSTeK.Computer.INC	Title : F5U	
	Engineer: He_Wang	
Size: Custom	Project Name: F5U	Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 57 of 88	



ASUSTeK Computer INC

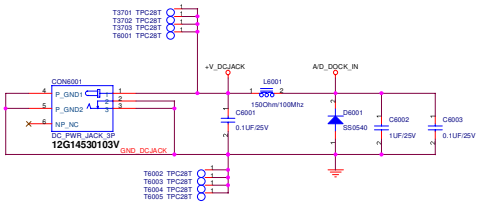
Title : F5U

Engineer: He_Wang

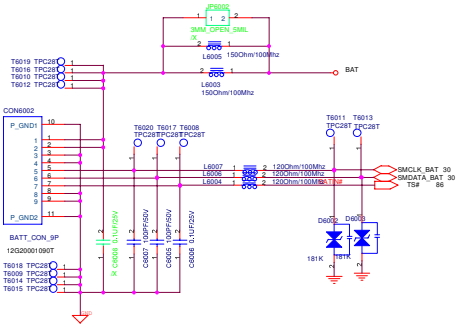
Size	Project Name	Rev
C	F5U	1.0

Date: 星期三, 十月 28, 2007 Sheet 59 of 88

Adaptor IN Circuit

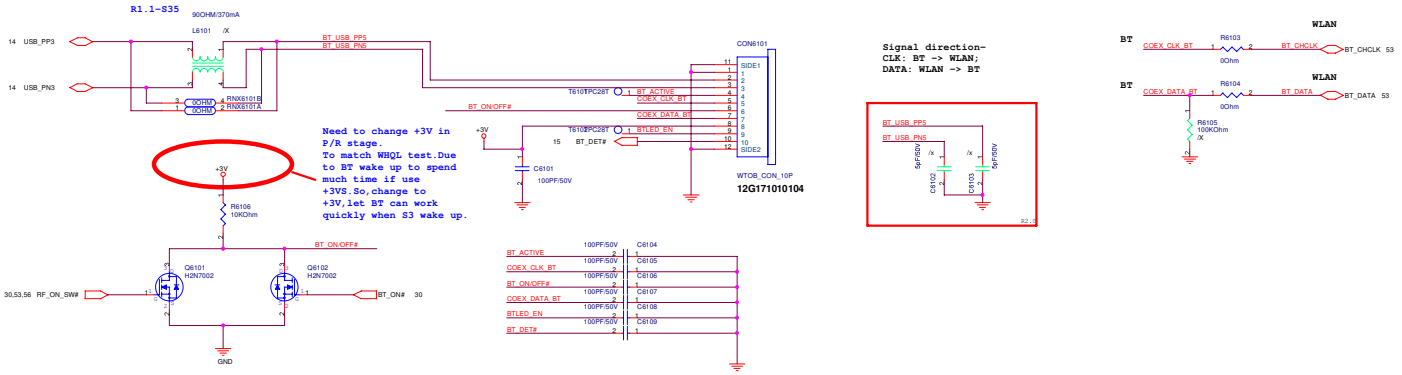


BATTERY IN CIRCUIT



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 60 of 88		

BLUETOOTH CONNECTOR



ASUSTeK Computer INC

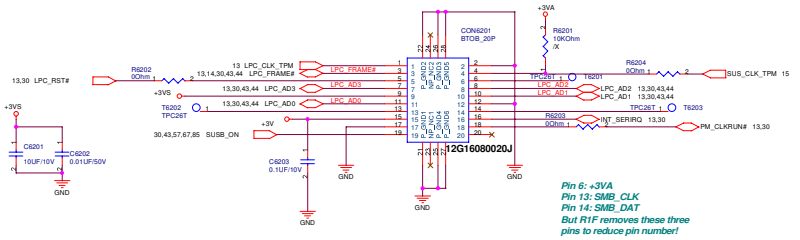
Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期二, 四月 09, 2007 Sheet 61 of 88

TPM CONN.



Pin 6: +3VA
Pin 13: SMB_CLK
Pin 14: SMB_DAT
But R1F removes these three pins to reduce pin number!

ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期一, 四月 09, 2007	Sheet 62 of 88		

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

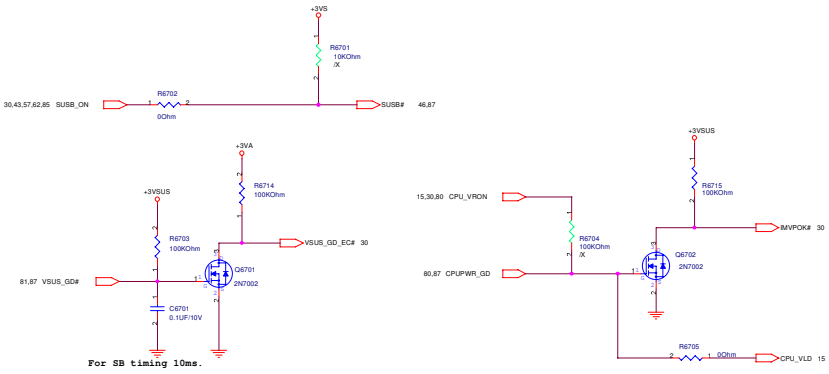
Size	Project Name	Rev
C	F5U	1.0

Date: 星期三, 十月 28, 2007 Sheet 63 of 88

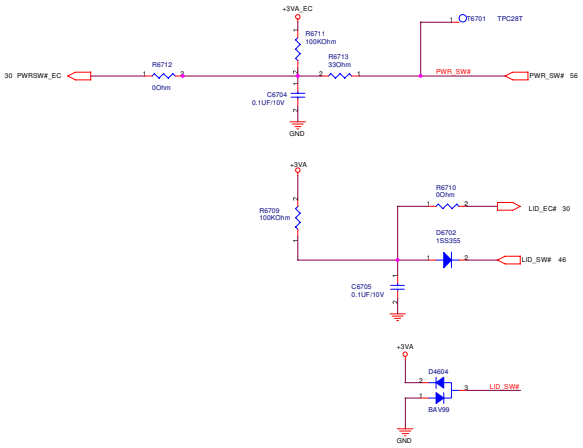
ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 64 of 88	

ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 65 of 88	

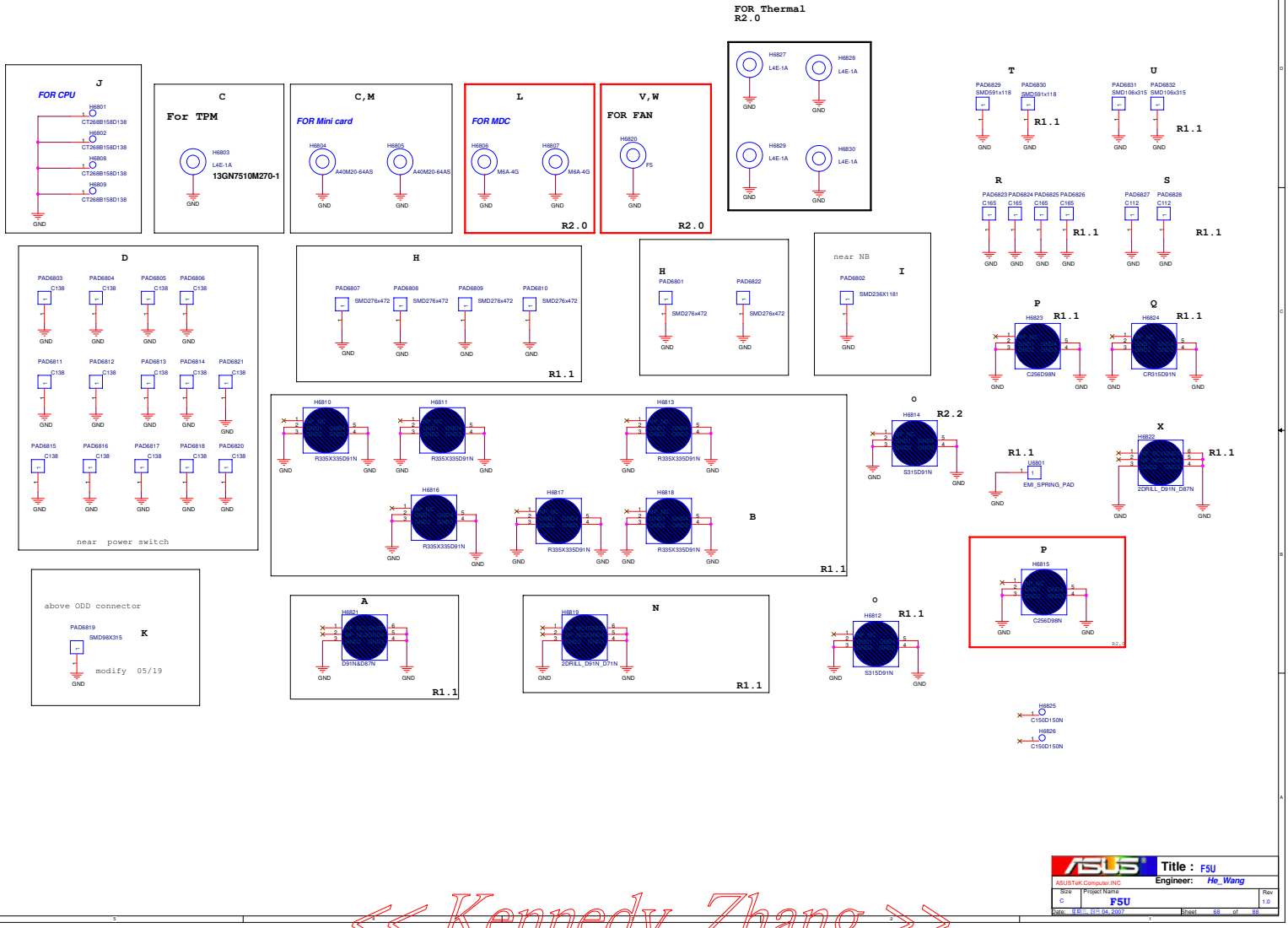
ASUSTeK Computer INC	Title : F5U	
	Engineer: He_Wang	
Size: C	Project Name: F5U	Rev: 1.0
Date: 星期三, 十月 28, 2007	Sheet 68 of 88	



POWER SWITCH



ASUSTeK Computer INC	Title : F5U		
	Engineer: He_Wang		
Size: C	Project Name: F5U		Rev: 1.0
Date: 星期二, 四月 09, 2007	Sheet 67 of 88		



ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期二, 四月 04, 2007 Sheet 68 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期三, 十月 28, 2007 Sheet 69 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期三, 十月 28, 2007 Sheet 70 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 71 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 72 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 73 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 74 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 75 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 78 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 77 of 88

ASUSTeK Computer INC

Title : F5U

Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

Date: 星期一, 十月 08, 2007 Sheet 78 of 88

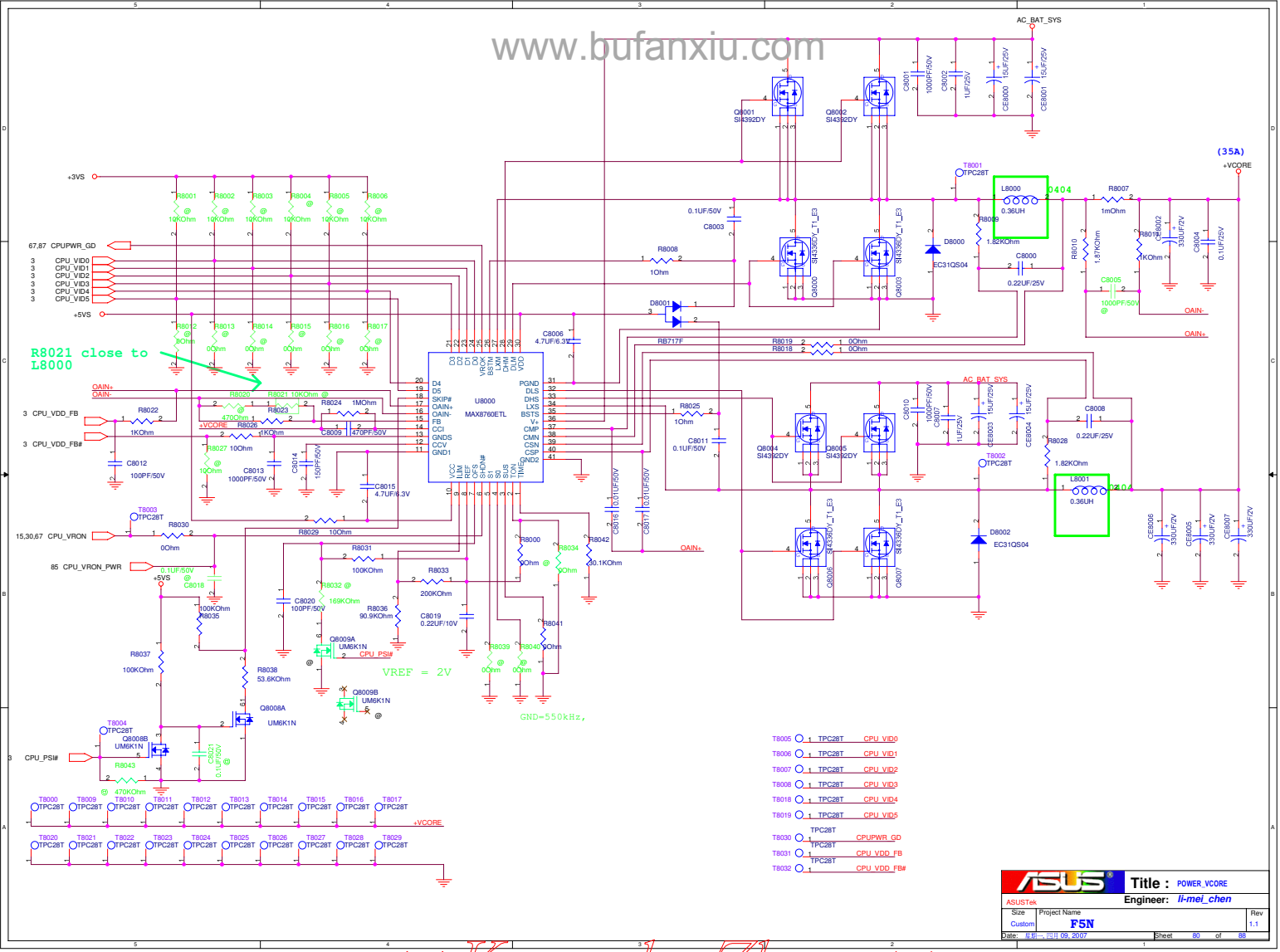
ASUSTeK Computer INC

Title : F5U

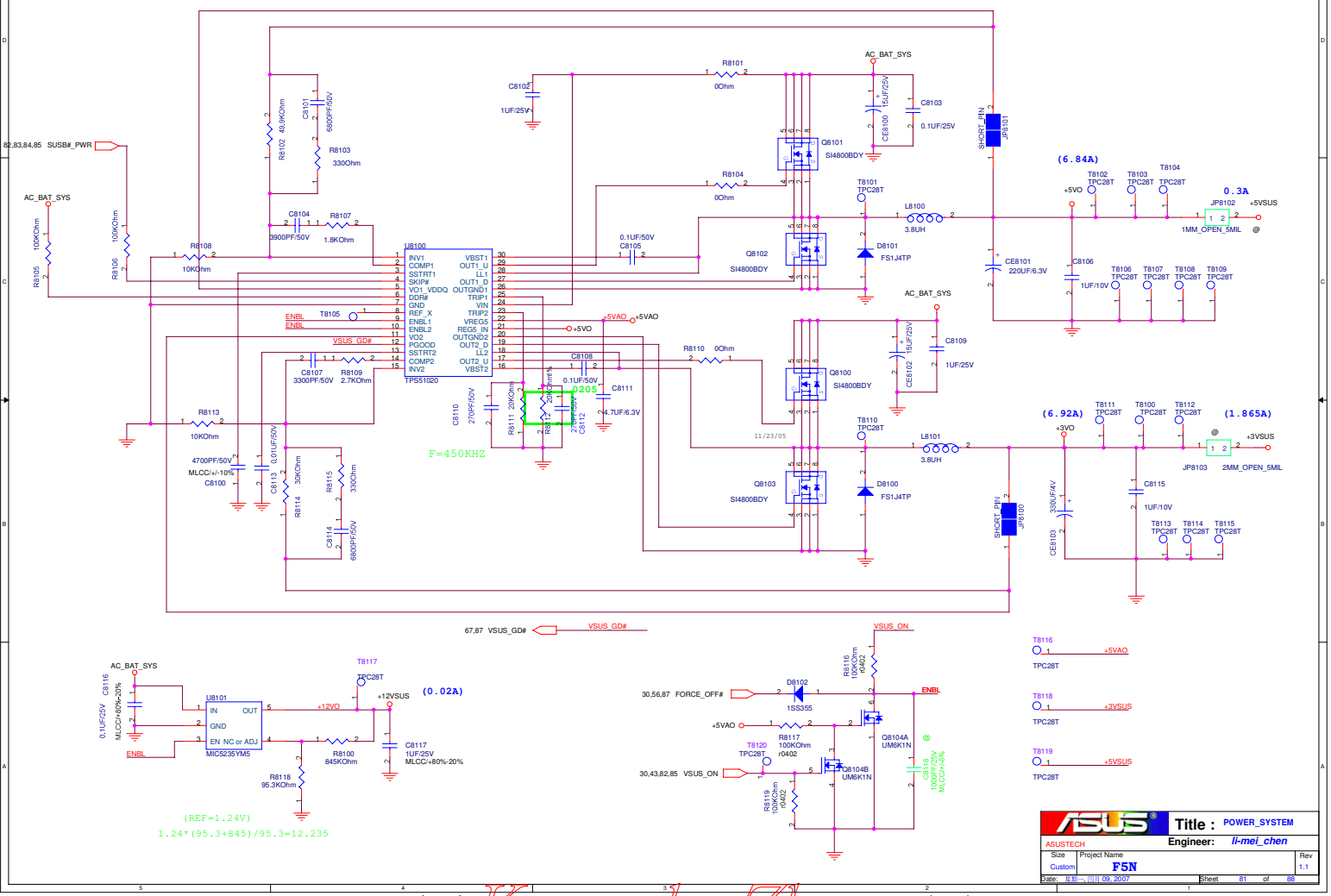
Engineer: He_Wang

Size	Project Name	Rev
C	F5U	1.0

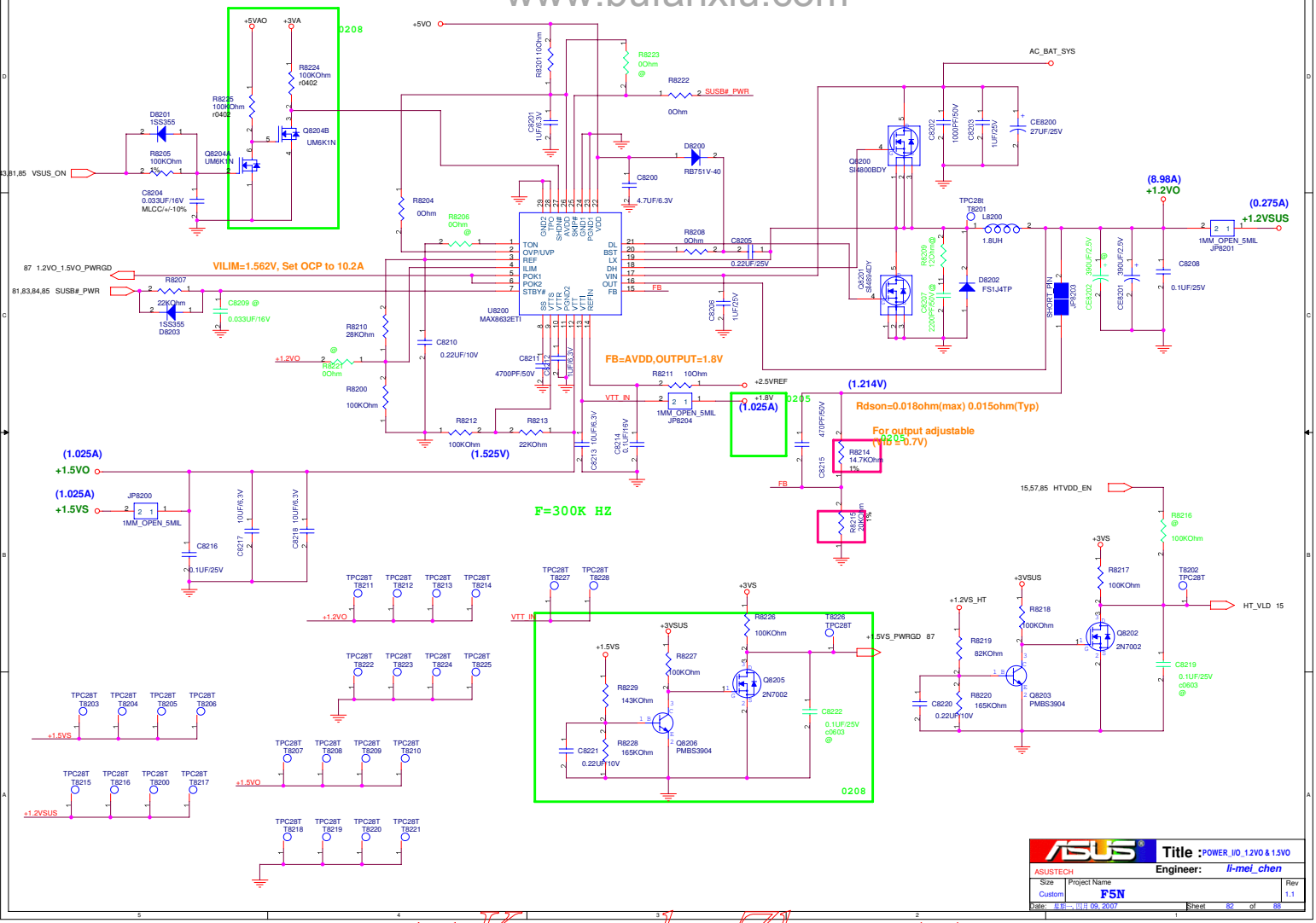
Date: 星期三, 十月 28, 2007 Sheet 79 of 88



ASUSTek	Title : POWER_VCORE		
	Engineer: li-mei_chen		
Size: Custom	Project Name: F5N		Rev: 1.1
Date: 星期一, 四月 09, 2007	Sheet 80 of 88		



ASUSTECH	Title : POWER_SYSTEM		
	Engineer: li-mei_chen		
Size: Custom	Project Name: F5N		Rev: 1.1
Date: 星期一, 四月 09, 2007		Sheet 81 of 88	



ASUSTECH

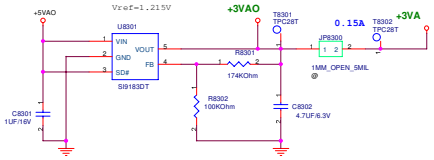
Title : POWER_I/O_1.2VO & 1.5VO

Engineer: li-mei_chen

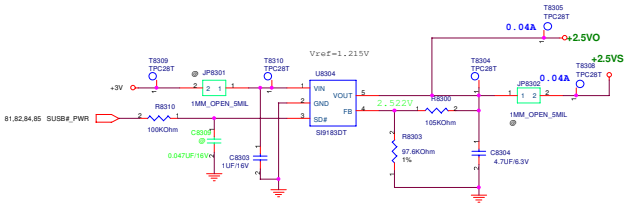
Size	Project Name	Rev
Custom	F5N	1.1

Date: 星期一, 四月 09, 2007 Sheet 82 of 88

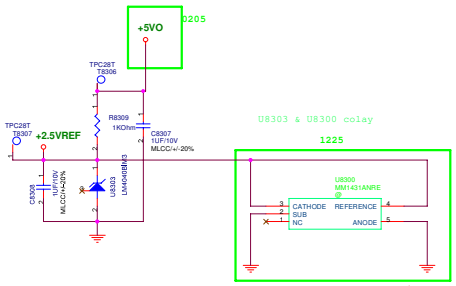
+3VA



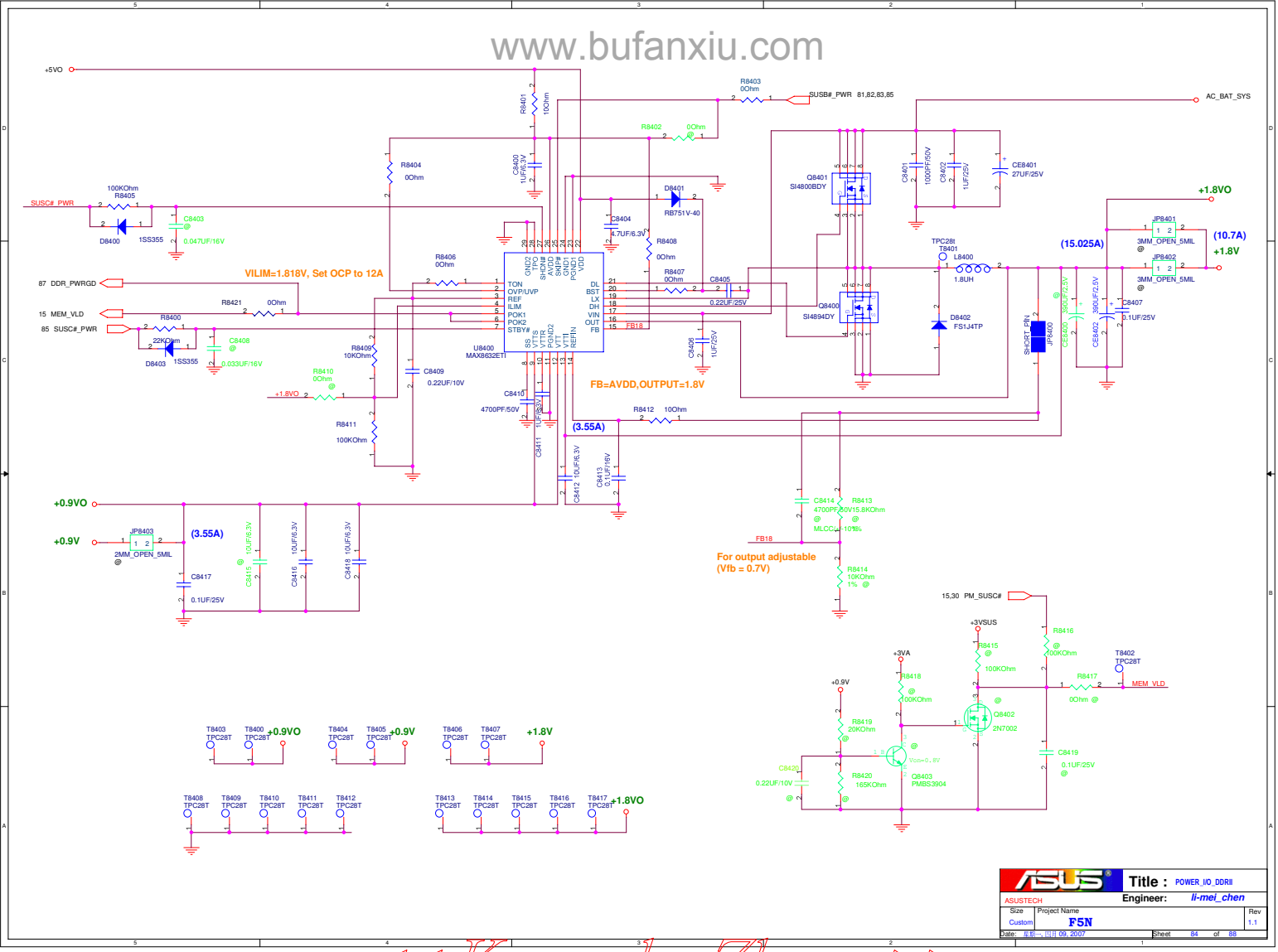
+2.5VS



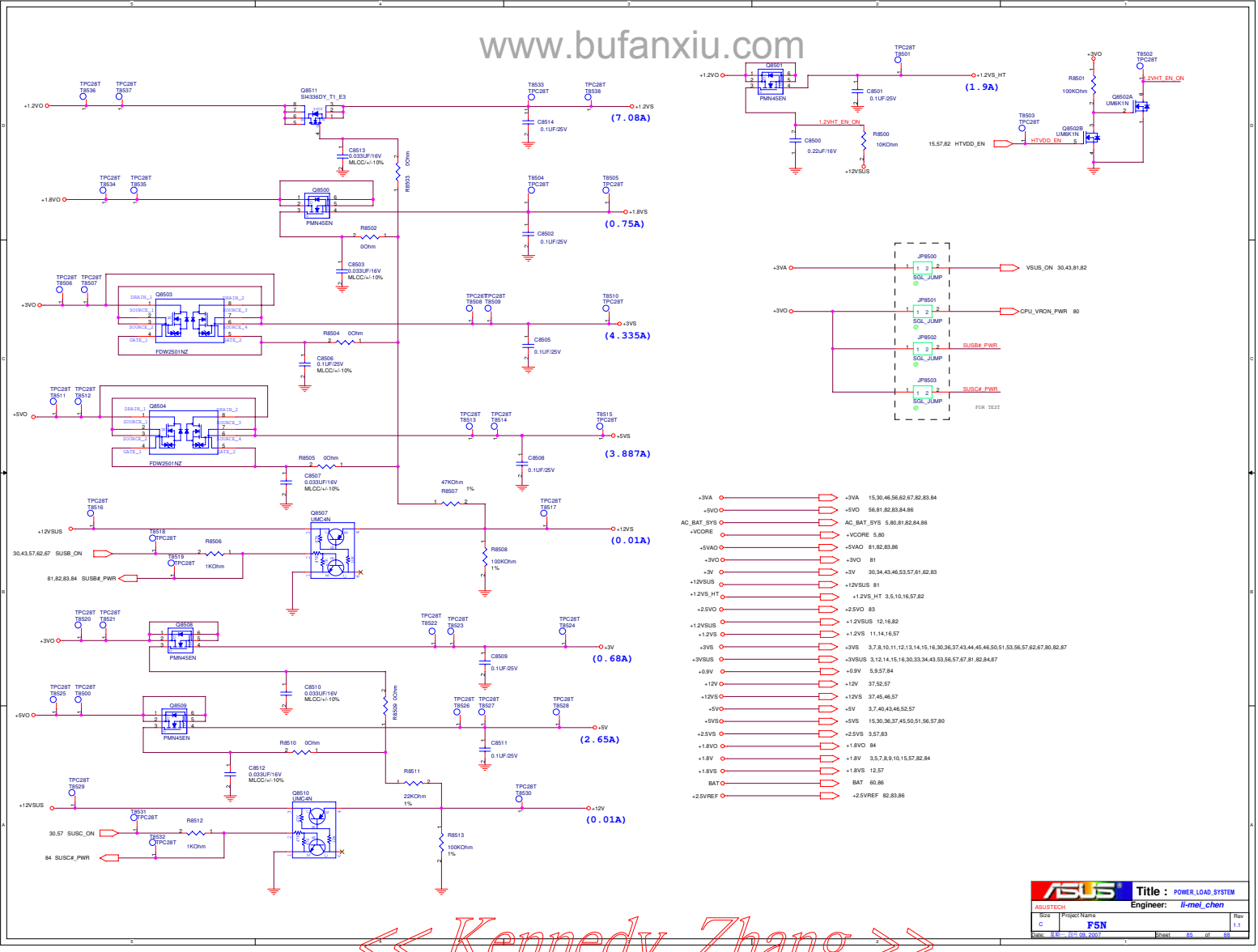
+2.5VREF



ASUSTECH	Title : POWER_I/O_LDO	
	Engineer: li-mei_chen	
Size: C	Project Name: F5N	Rev: 1.1
Date: 星期二, 四月 09, 2007	Sheet 83 of 88	



ASUSTECH	Title : POWER_I/O_DDRII		
	Engineer: li-mei_chen		
Size: Custom	Project Name: F5N		Rev: 1.1
Date: 星期一, 四月 09, 2007	Sheet 84 of 88		



+3VA	+3VA	15,30,46,56,62,67,82,83,84
+5VO	+5VO	56,81,82,83,84,86
AC_BAT_SYS	AC_BAT_SYS	5,80,81,82,84,86
+VCORE	+VCORE	5,80
+5VAO	+5VAO	81,82,83,86
+3VO	+3VO	81
+3V	+3V	30,34,43,46,53,57,61,62,83
+12VSUS	+12VSUS	81
+1.2VS_HT	+1.2VS_HT	3,5,10,16,57,82
+2.5VO	+2.5VO	83
+1.2VSUS	+1.2VSUS	12,16,82
+1.2VS	+1.2VS	11,14,16,57
+3VS	+3VS	3,7,8,10,11,12,13,14,15,16,30,36,37,43,44,45,46,50,51,53,56,57,62,67,80,82,87
+3VSUS	+3VSUS	3,12,14,15,16,30,33,34,43,53,56,57,67,81,82,84,87
+0.9V	+0.9V	5,9,57,84
+12V	+12V	37,52,57
+12VS	+12VS	37,45,46,57
+5VO	+5V	3,7,40,43,46,52,57
+5VS	+5VS	15,30,36,37,45,50,51,56,57,80
+2.5VS	+2.5VS	3,57,83
+1.8VO	+1.8VO	84
+1.8V	+1.8V	3,5,7,8,9,10,15,57,82,84
+1.8VS	+1.8VS	12,57
BAT	BAT	60,86
+2.5VREF	+2.5VREF	82,83,86

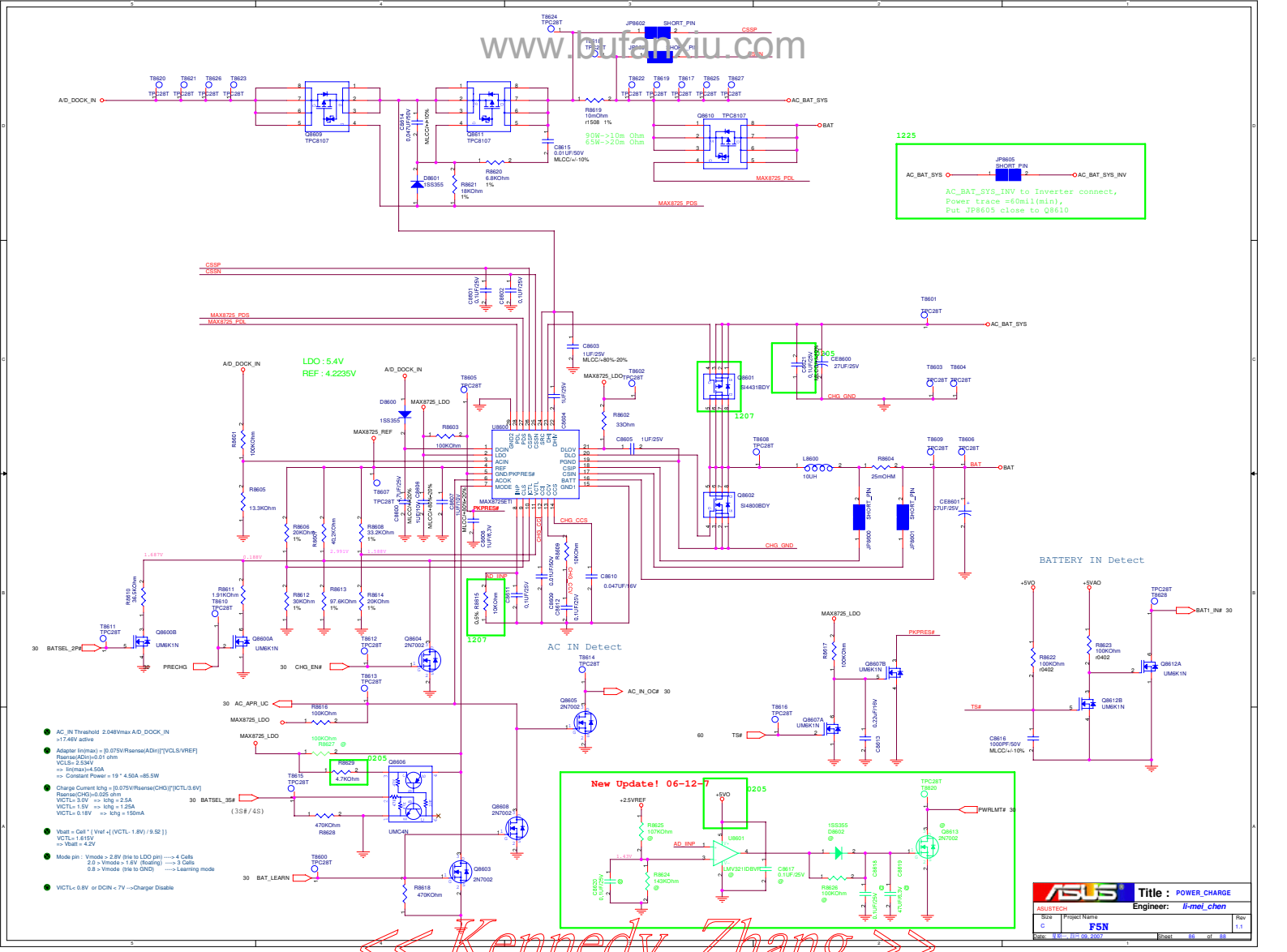
ASUSTeK

Title : POWER_LOAD_SYSTEM

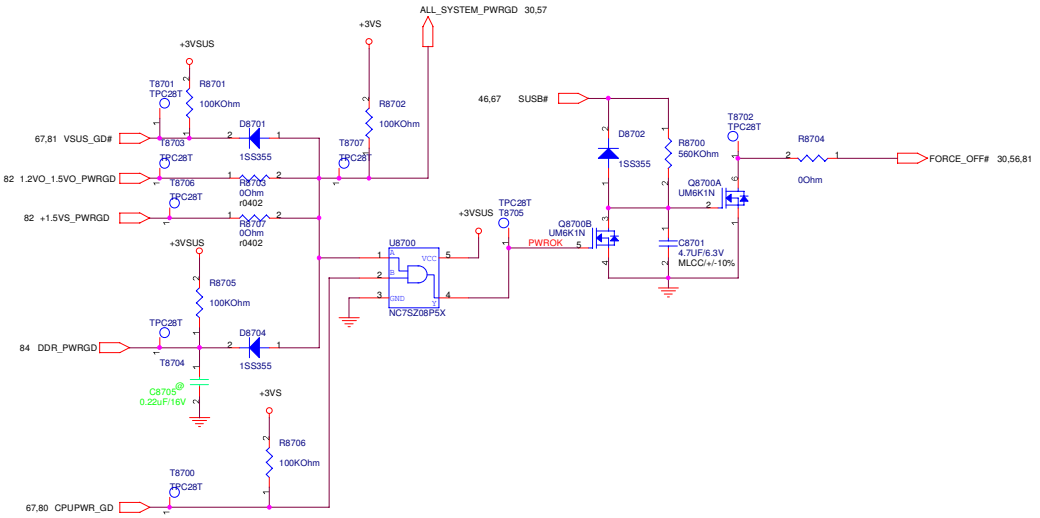
Engineer: li-mei_chen

Size	Project Name	Rev
C	F5N	1.1

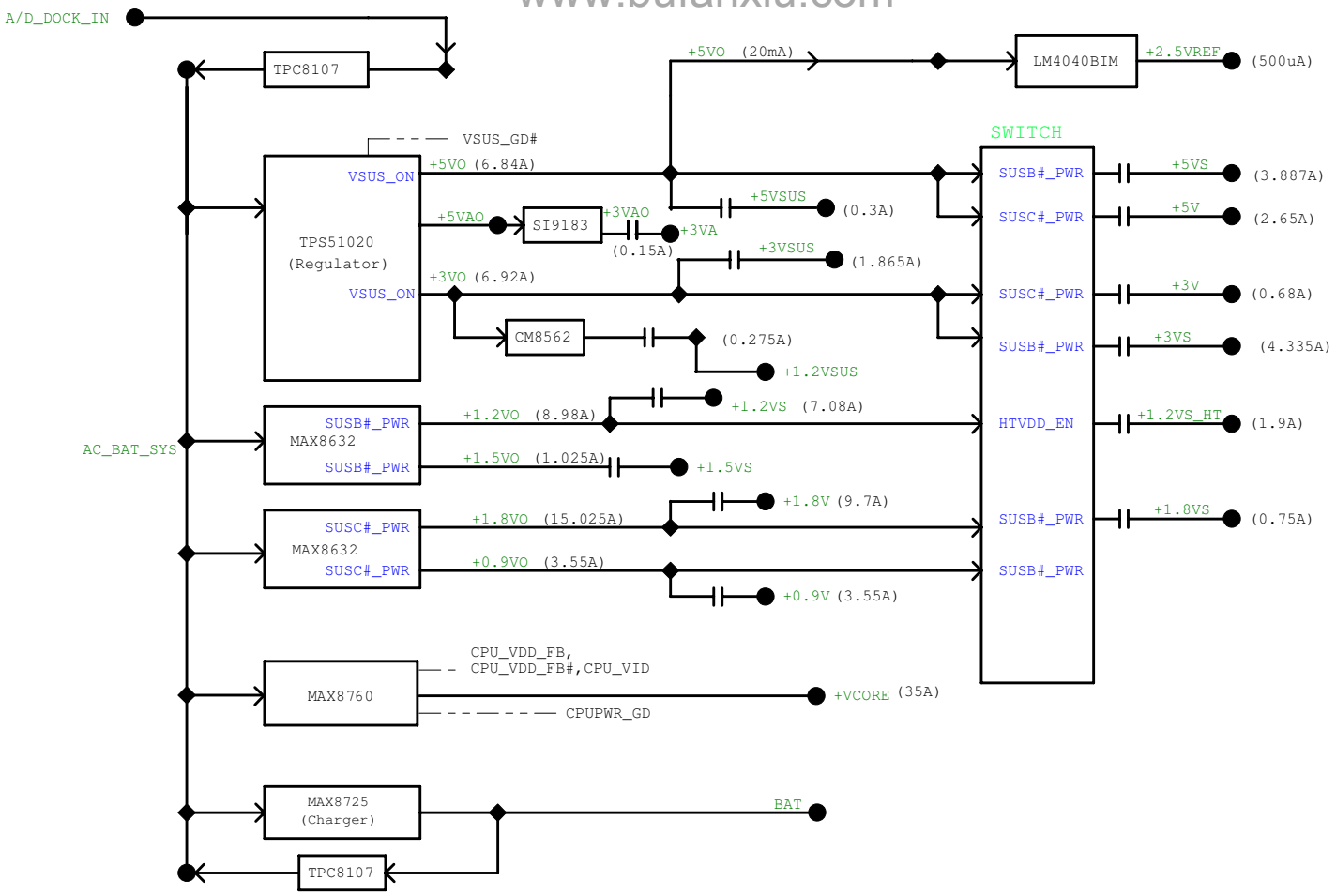
Date: 星期一, 四月 09, 2007 Sheet 85 of 88



- AC_IN Thrashold 2.048Vmax A/D_DOCK_IN
  >17.46V active
- Adapter Iin(max) = [0.075V/Rsense(ADin)]*[VCLS/VREF]
  Rsense(ADin)=0.01 ohm
  VCLS= 2.534V
  => Iin(max)=4.50A
  => Constant Power = 19 * 4.50A =85.5W
- Charge Current Ichg = [0.075V/Rsense(CHG)]*[ICTL/3.6V]
  Rsense(CHG)=0.025 ohm
  VICTL= 3.0V => Ichg = 2.5A
  VICTL= 1.5V => Ichg = 1.25A
  VICTL= 0.18V => Ichg = 150mA
- Vbatt = Cell * (Vref +[(VCTL- 1.8V) / 9.52])
  VCTL= 1.615V
  => Vbatt = 4.2V
- Mode pin : Vmode > 2.8V (trie to LDO pin) ----> 4 Cells
  2.0 > Vmode > 1.8V (floating) ----> 3 Cells
  0.8 > Vmode (trie to GND) ----> Learning mode
- VICTL< 0.8V or DCIN < 7V -->Charger Disable



ASUSTECH	Title : POWER_PROTECT	
	Engineer: li-mei_chen	
Size: Custom	Project Name: F5N	Rev: 1.1
Date: 星期一, 四月 09, 2007	Sheet 87 of 88	



ASUSTECH

Title : POWER DIAGRAM

Engineer: li-mei_chen

Size	Project Name	Rev
Custom	F5N	1.1

Date: 星期一, 四月 09, 2007 Sheet 88 of 88